

**FIG. 1A**  
**(RELATED ART)**

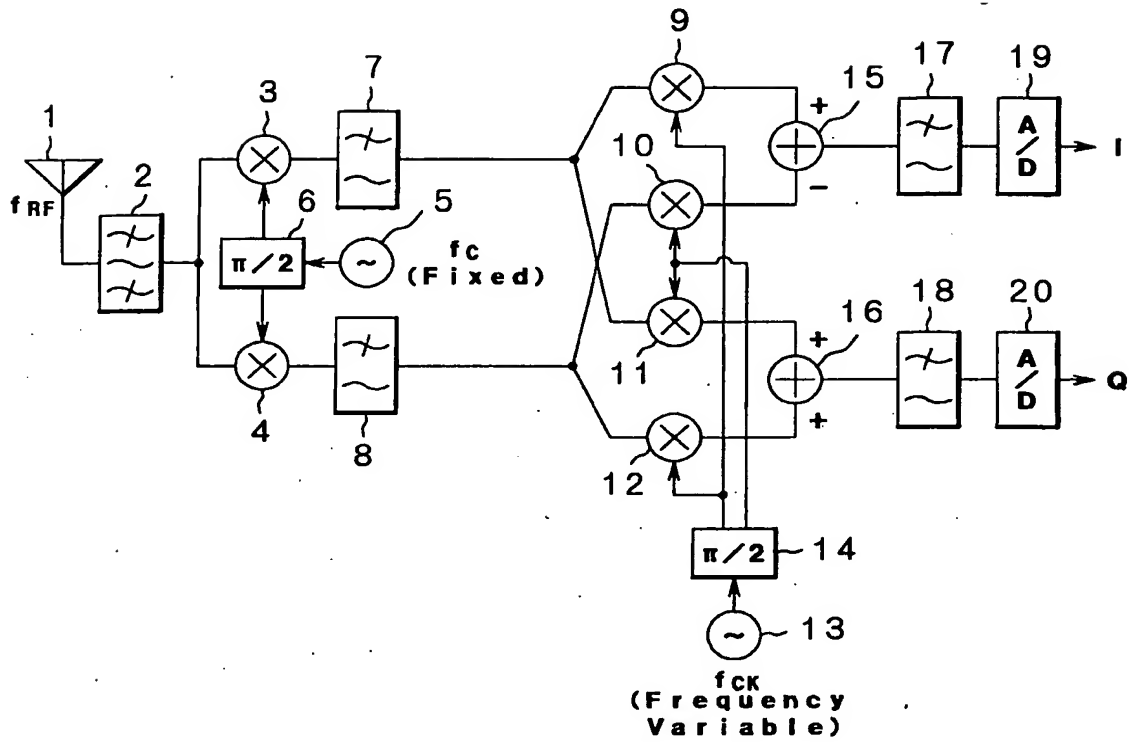


FIG.1B  
(RELATED ART)

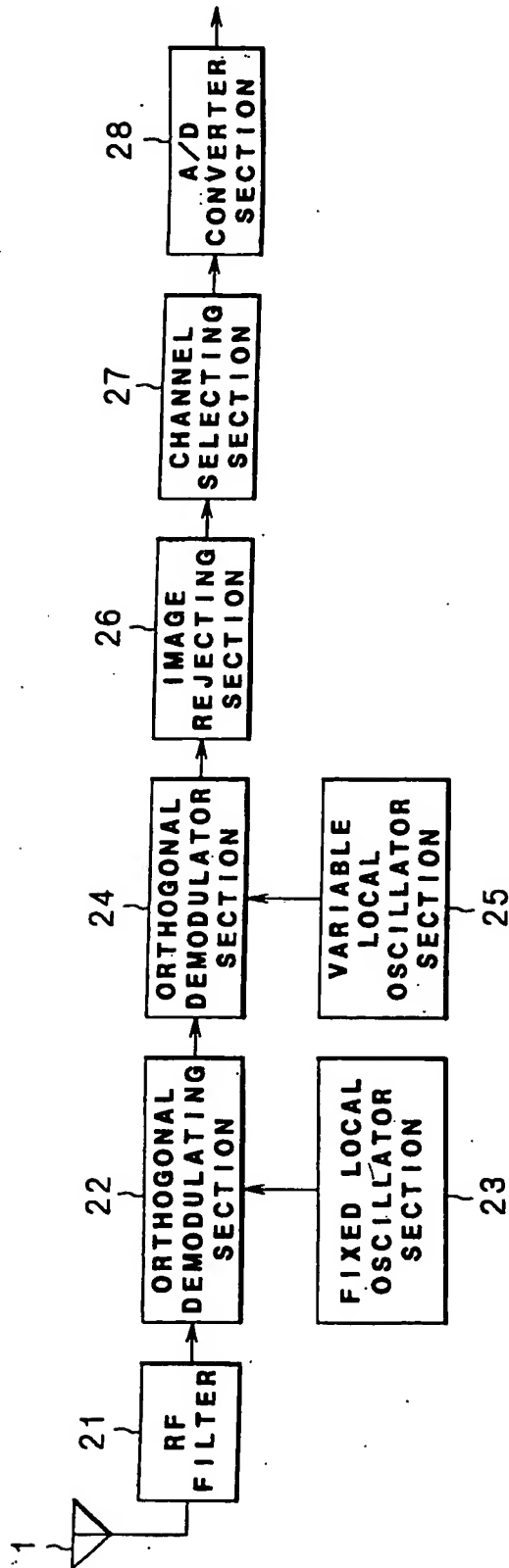


FIG.2A  
(RELATED ART)

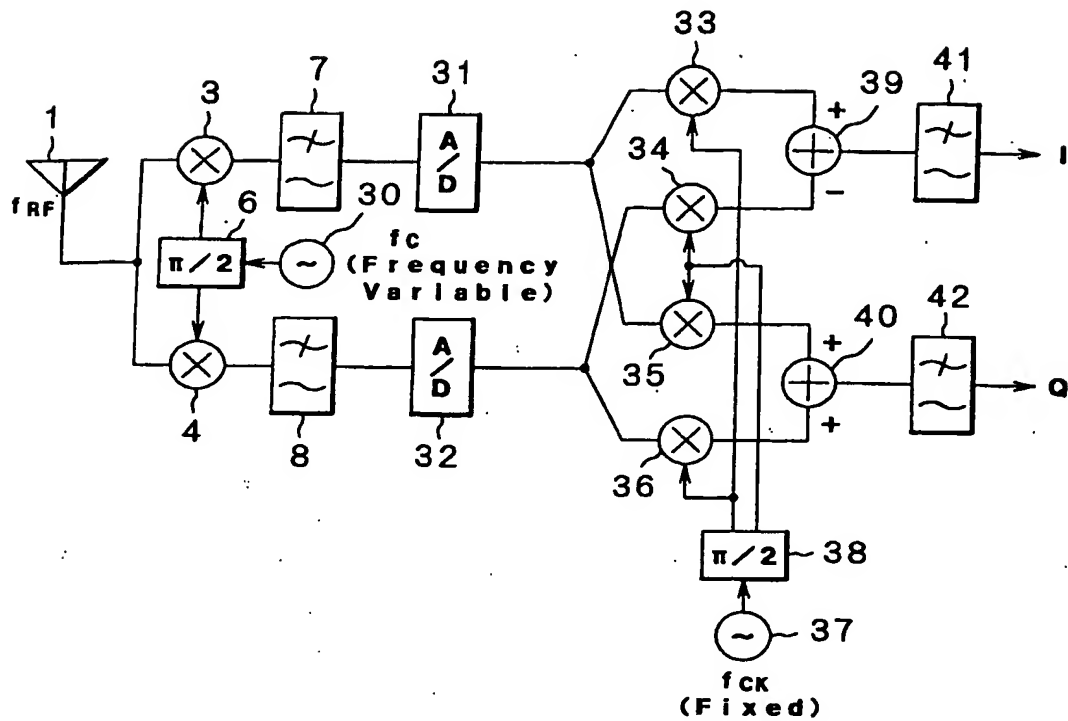
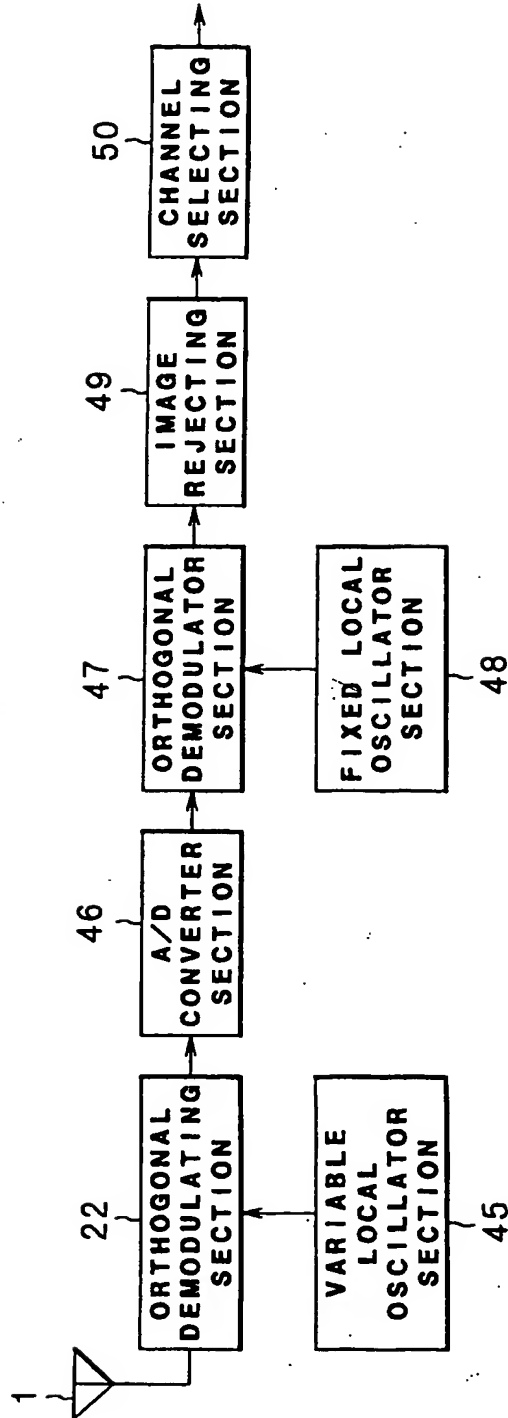


FIG.2B  
(RELATED ART)



**FIG.3A**  
**(RELATED ART)**

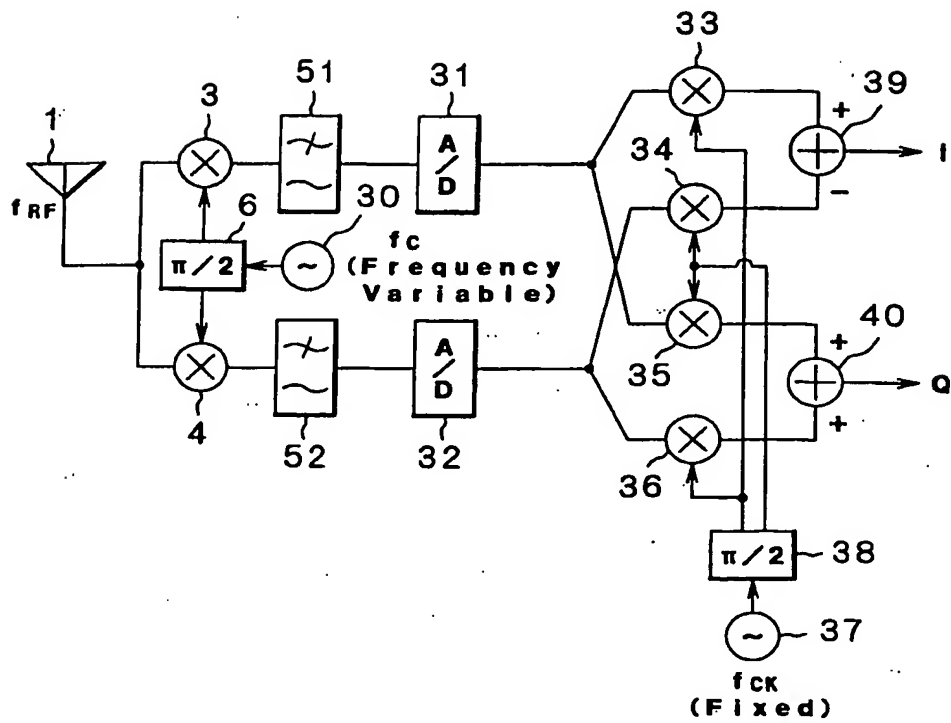


FIG.3B  
(RELATED ART)

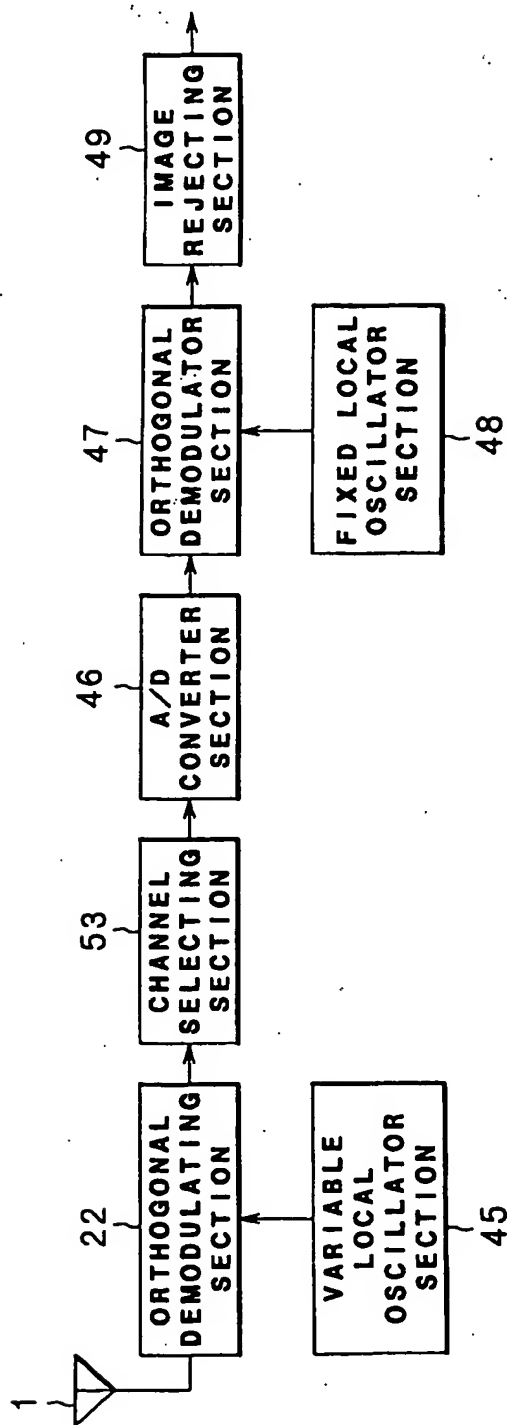
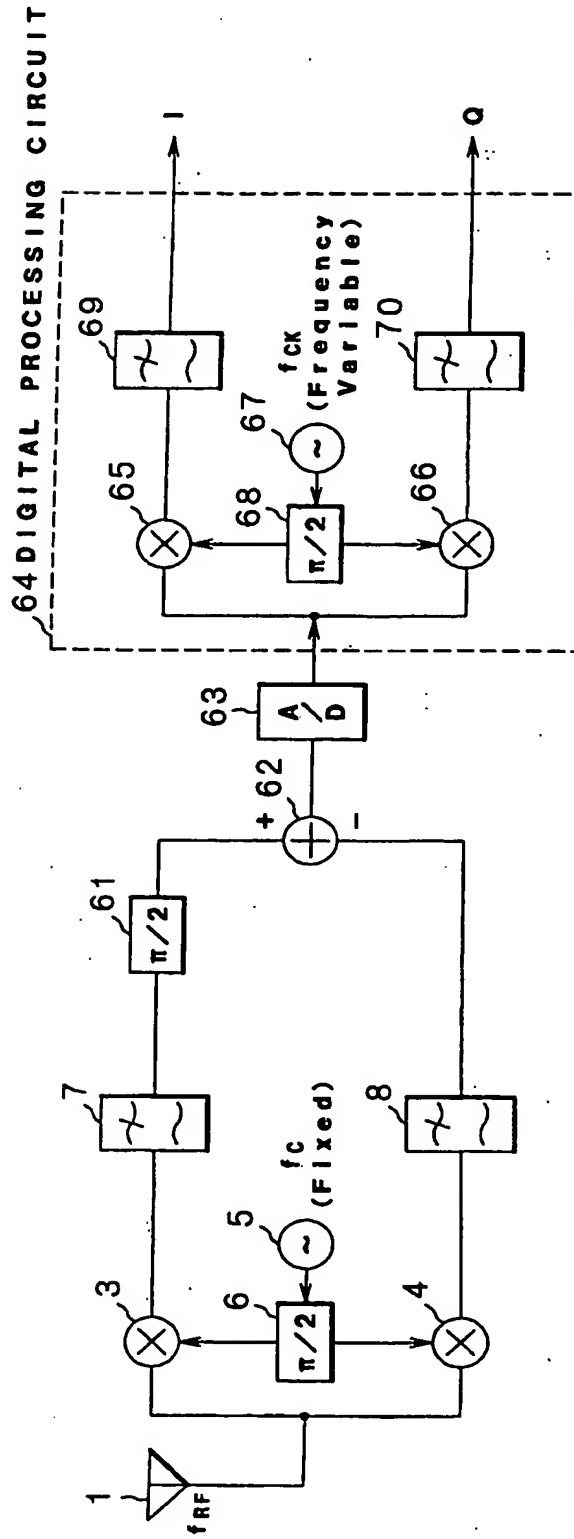
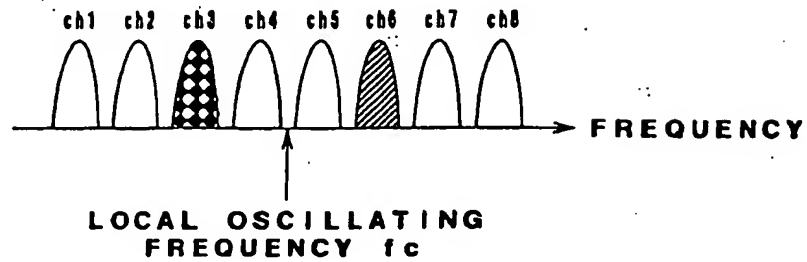


FIG.4  
(RELATED ART)

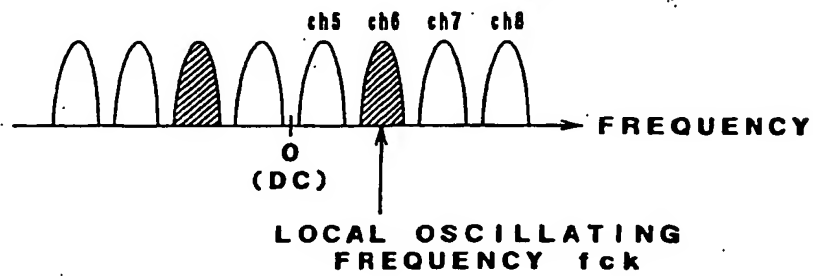


**FIG.5A**  
(RELATED ART)



ANALOG ORTHOGONAL  
DEMODULATION

**FIG.5B**  
(RELATED ART)



DIGITAL ORTHOGONAL  
DEMODULATION

**FIG.5C**  
(RELATED ART)

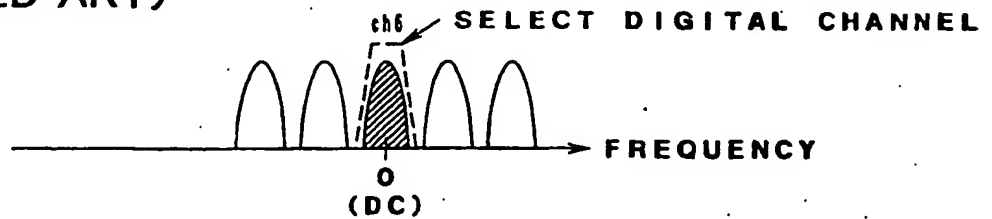
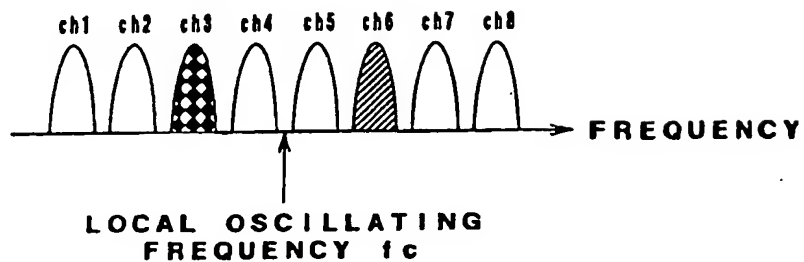


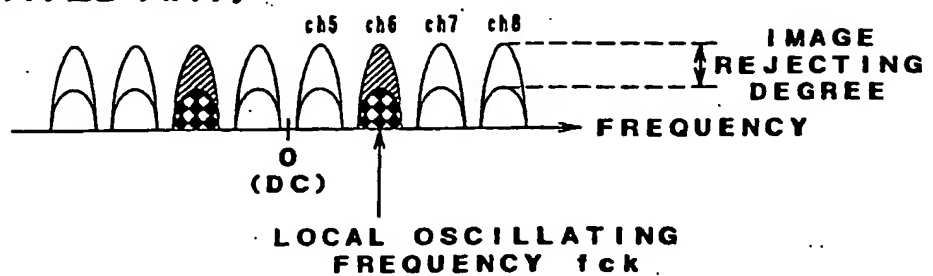


FIG.6A  
(RELATED ART)



ORTHOGONAL  
DEMODULATION

FIG.6B  
(RELATED ART)



A/D CONVERSION,  
DIGITAL ORTHOGONAL  
DEMODULATION

FIG.6C  
(RELATED ART)

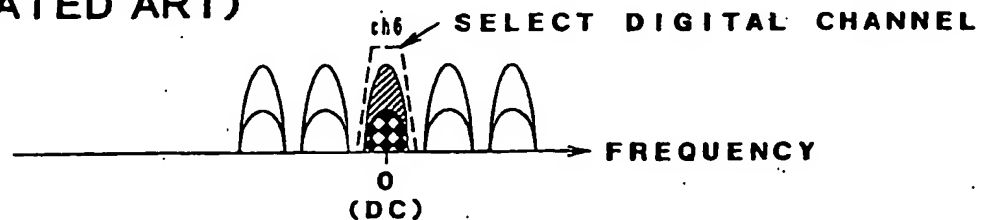


FIG. 7

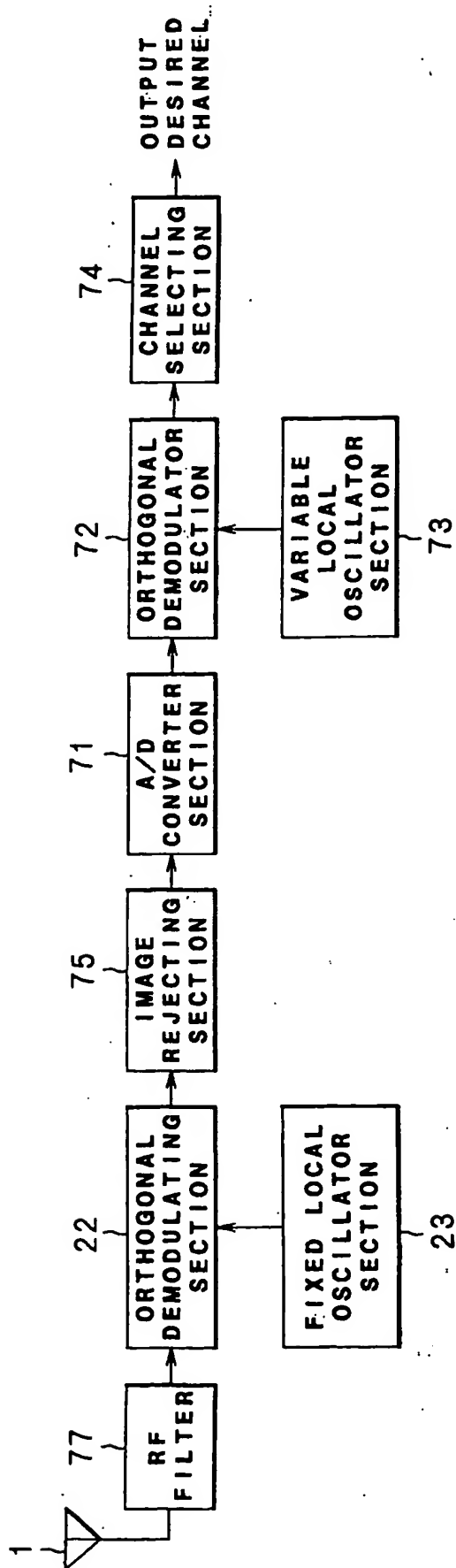


FIG. 8

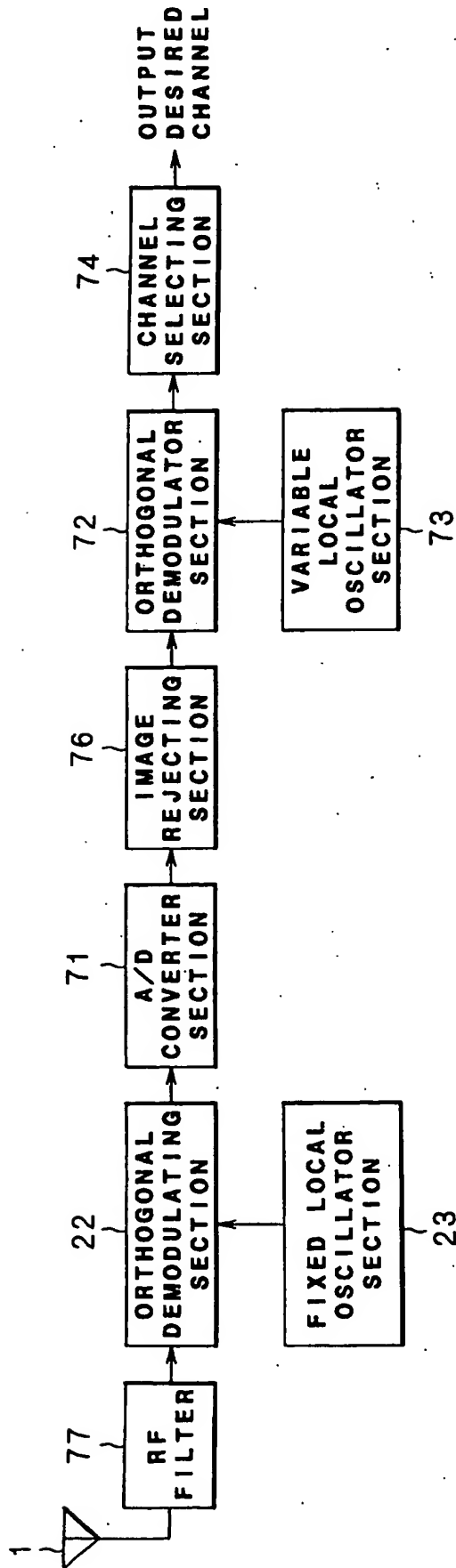


FIG. 9

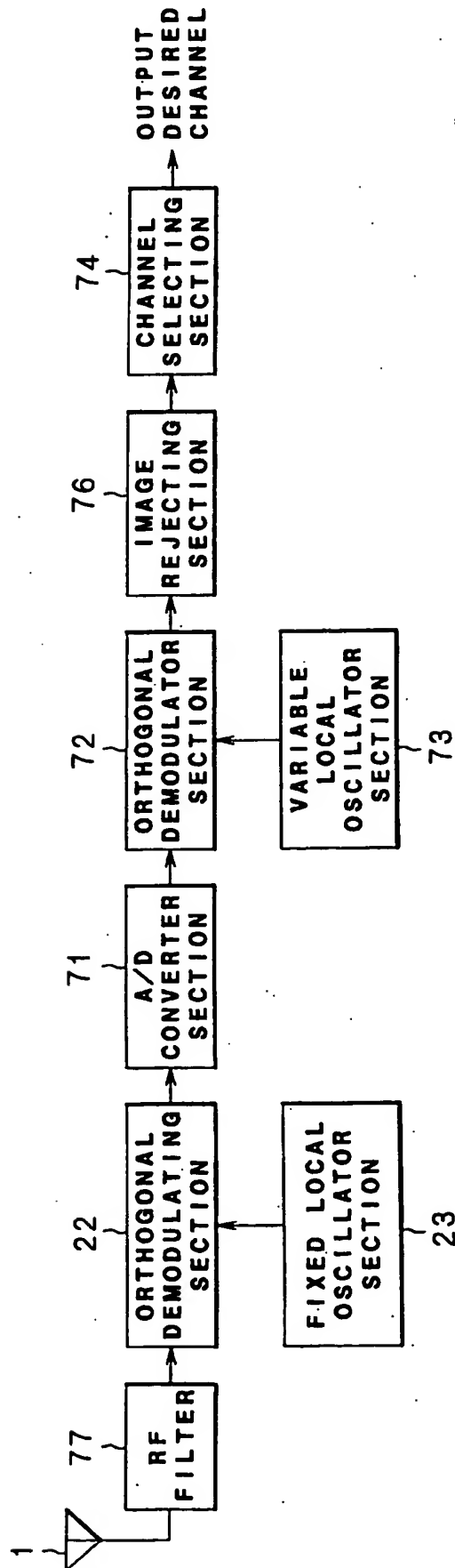


FIG.10

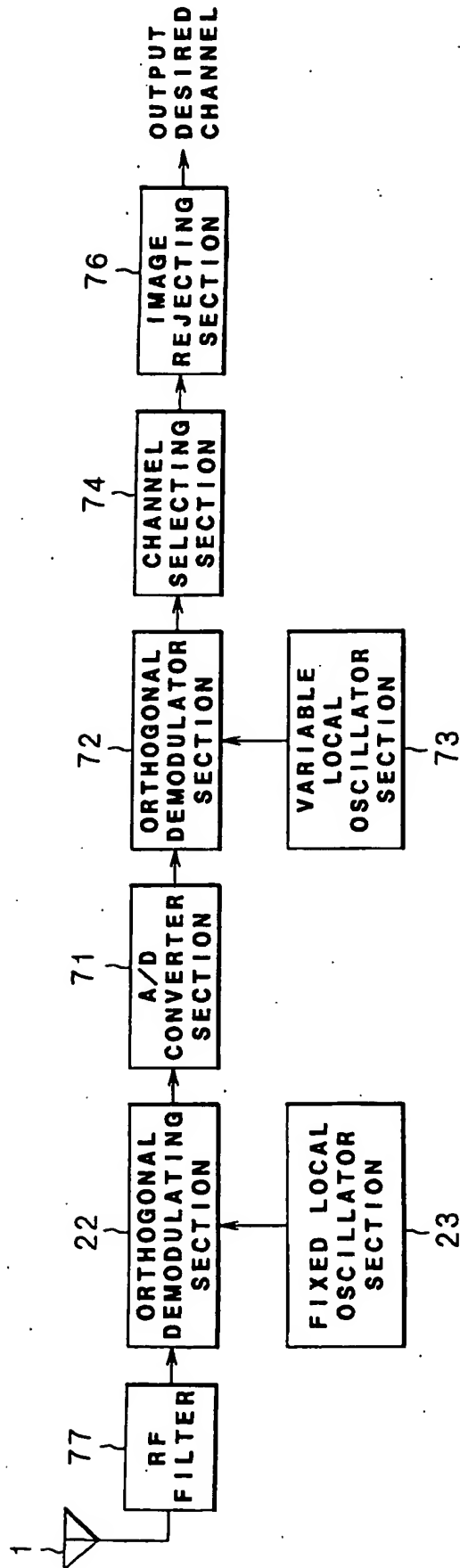


FIG.11A

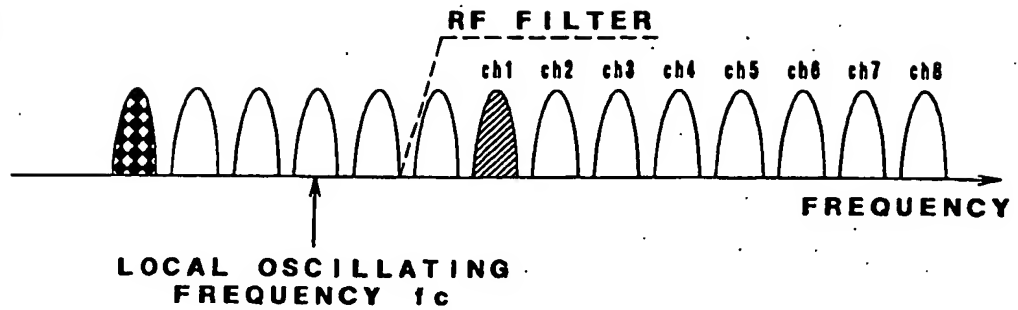


FIG.11B

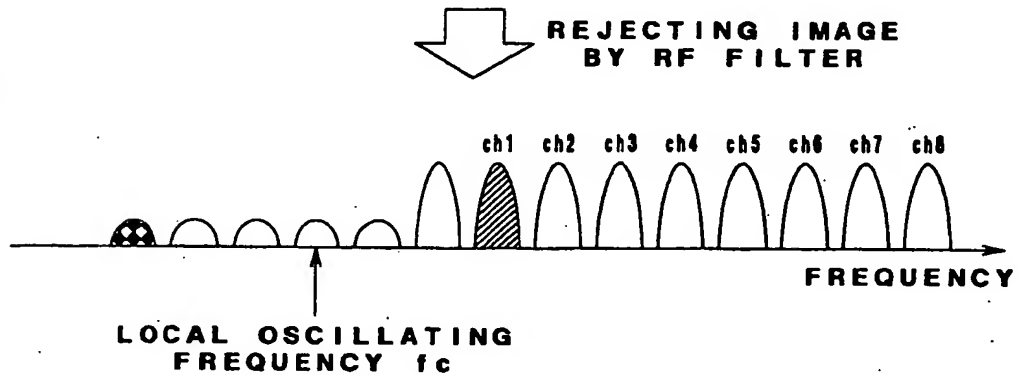


FIG.11C

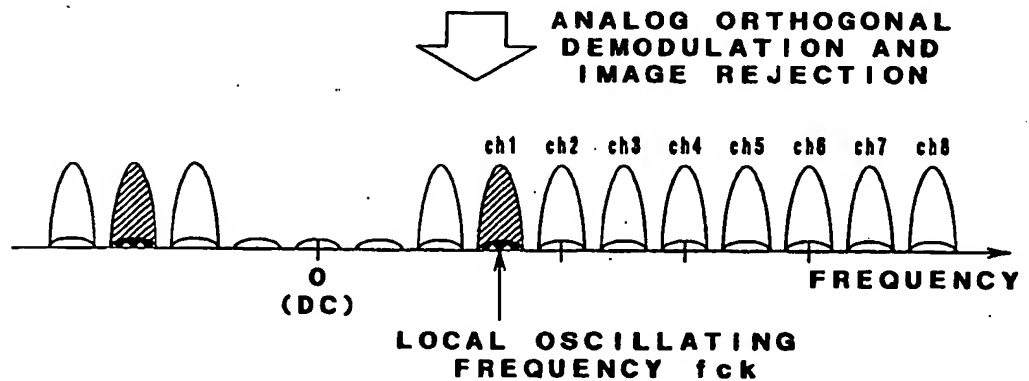


FIG.11D

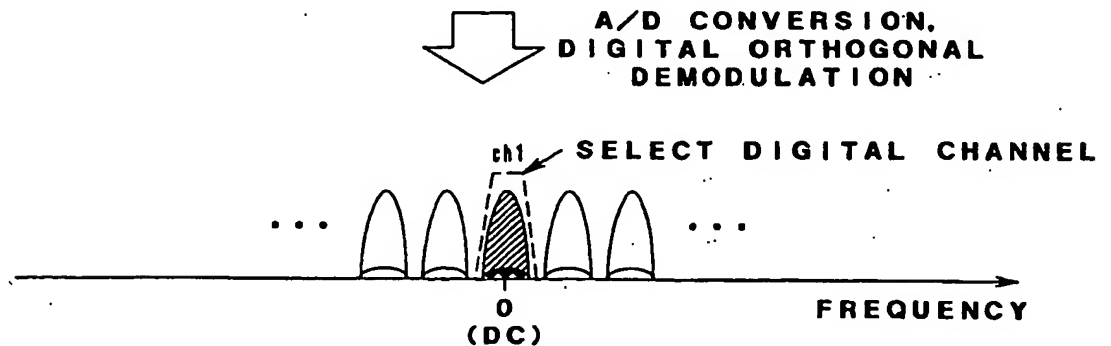


FIG.12

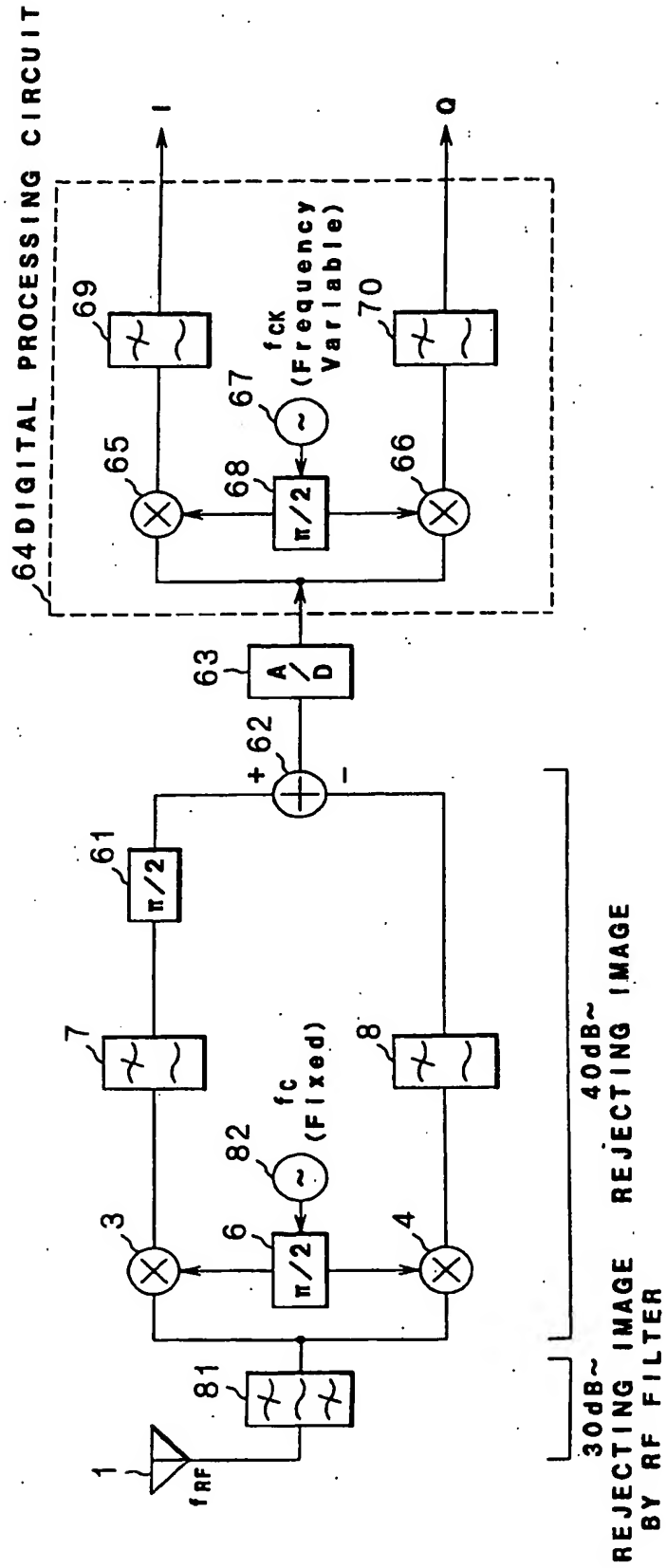


FIG.13

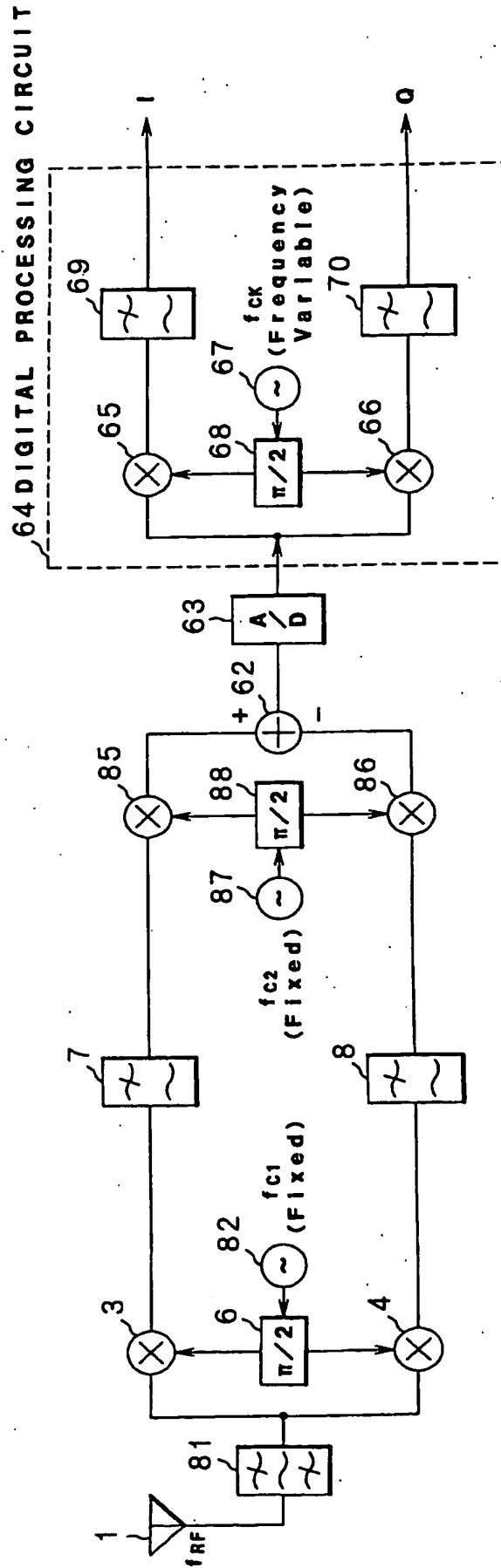




FIG.14

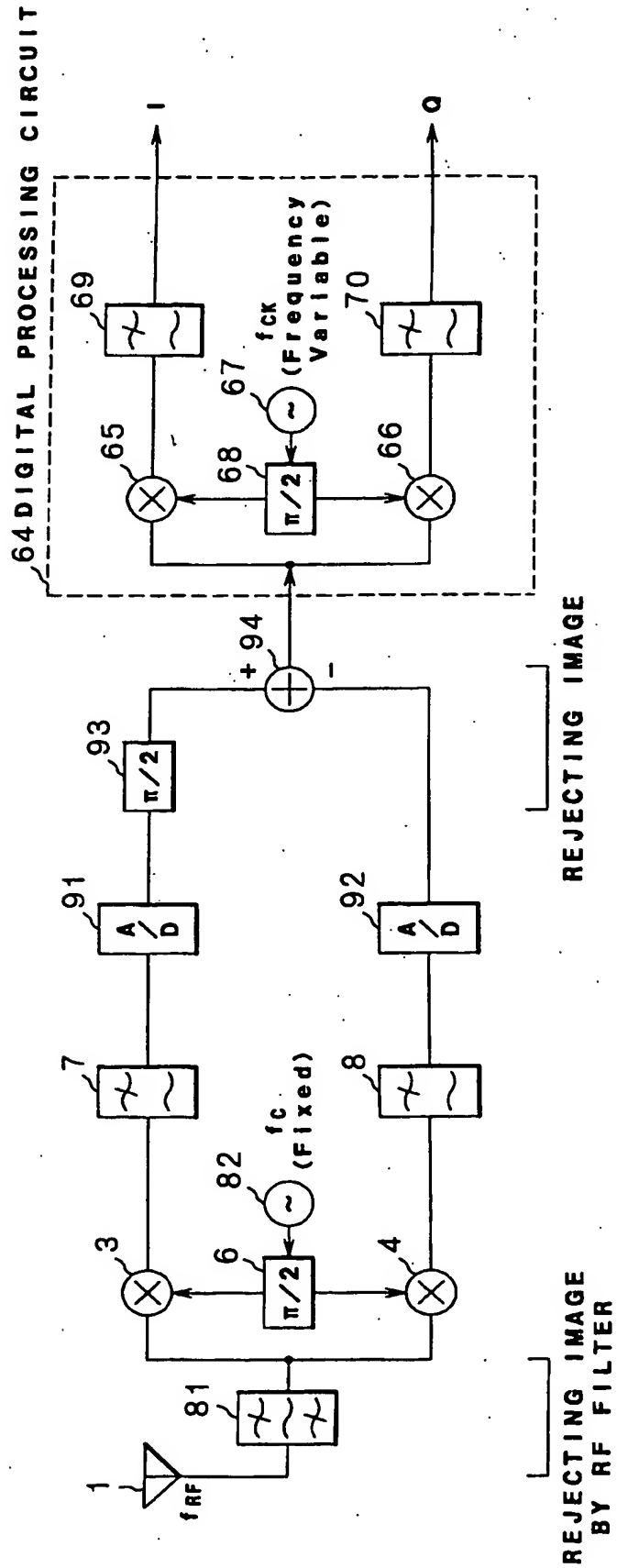


FIG.15

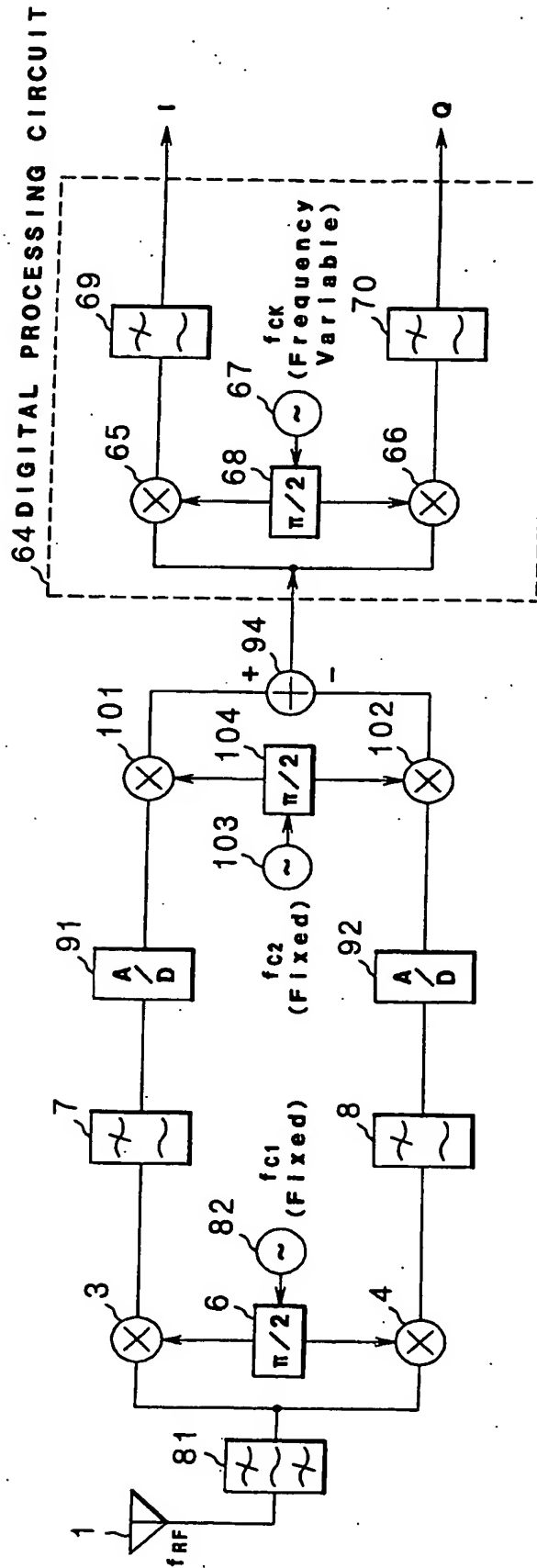


FIG.16

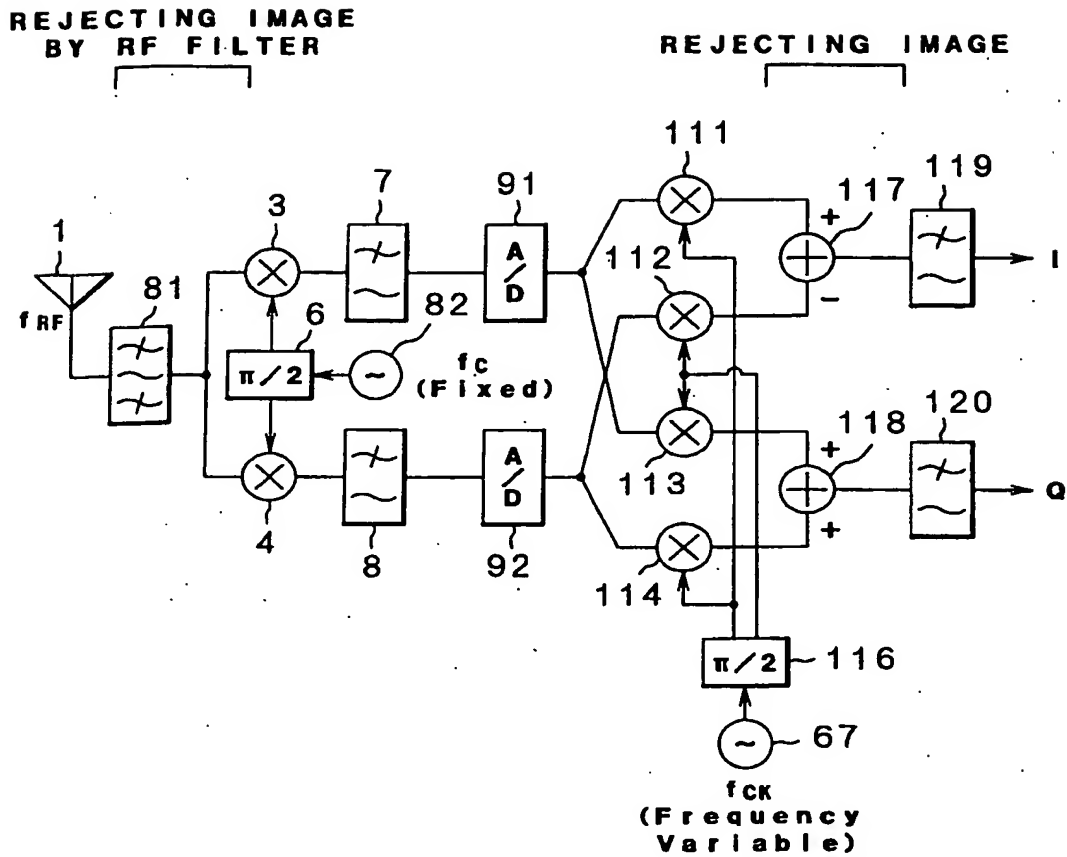


FIG.17

REJECTING IMAGE  
BY RF FILTER

REJECTING IMAGE

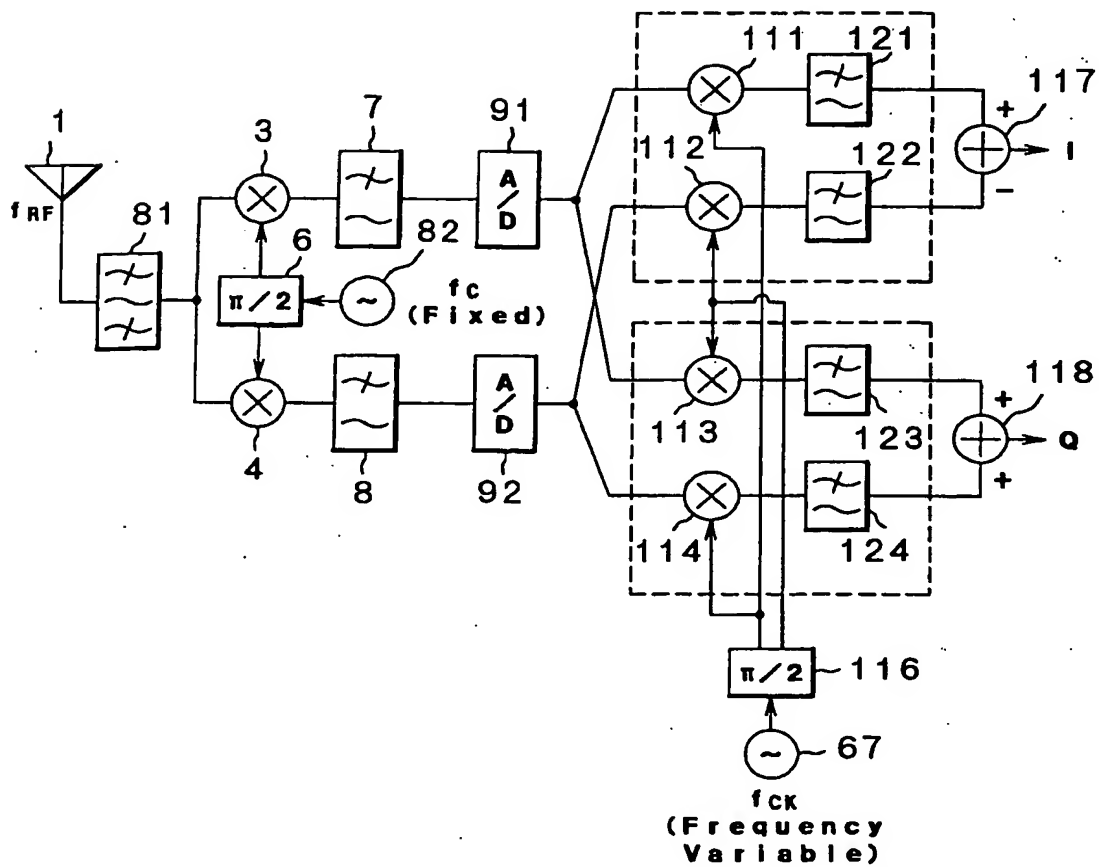


FIG.18

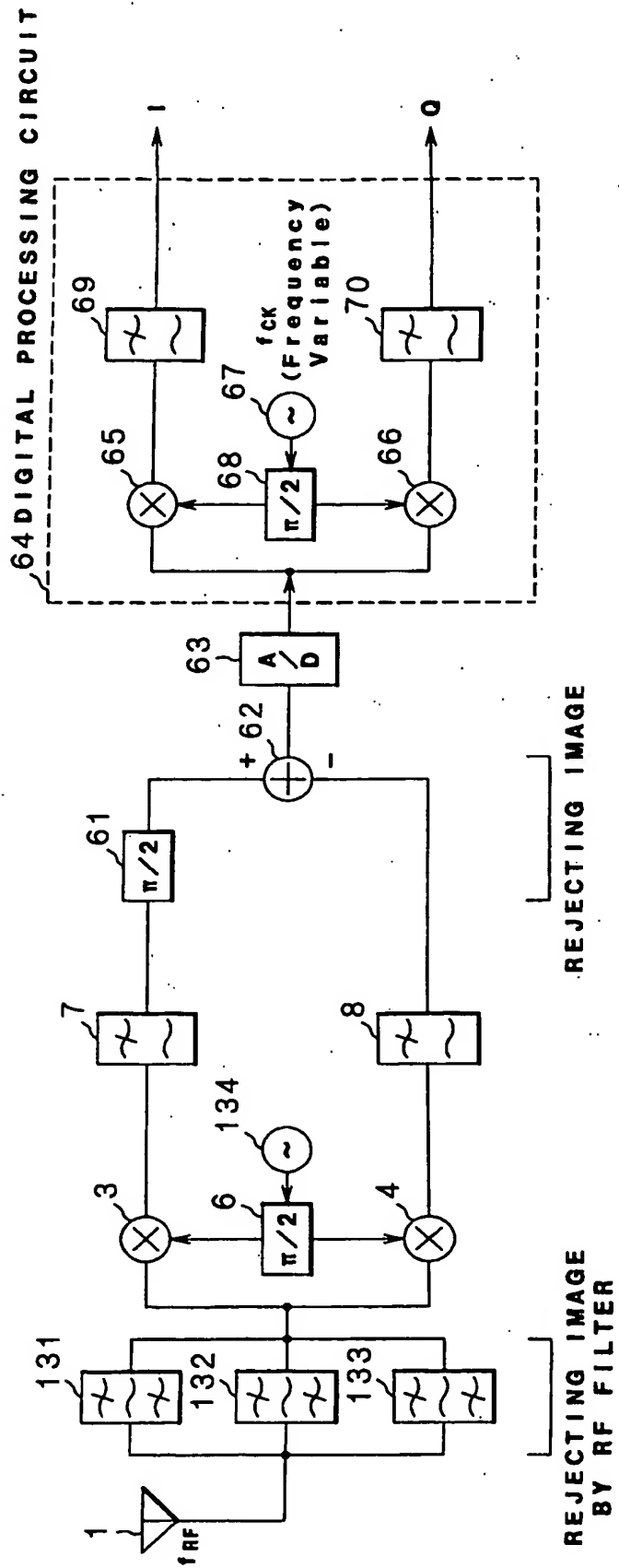


FIG.19

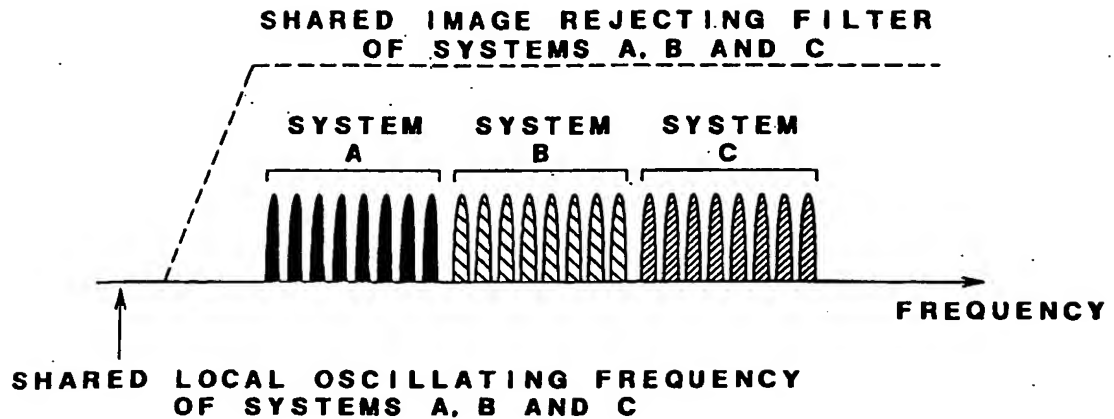


FIG.20

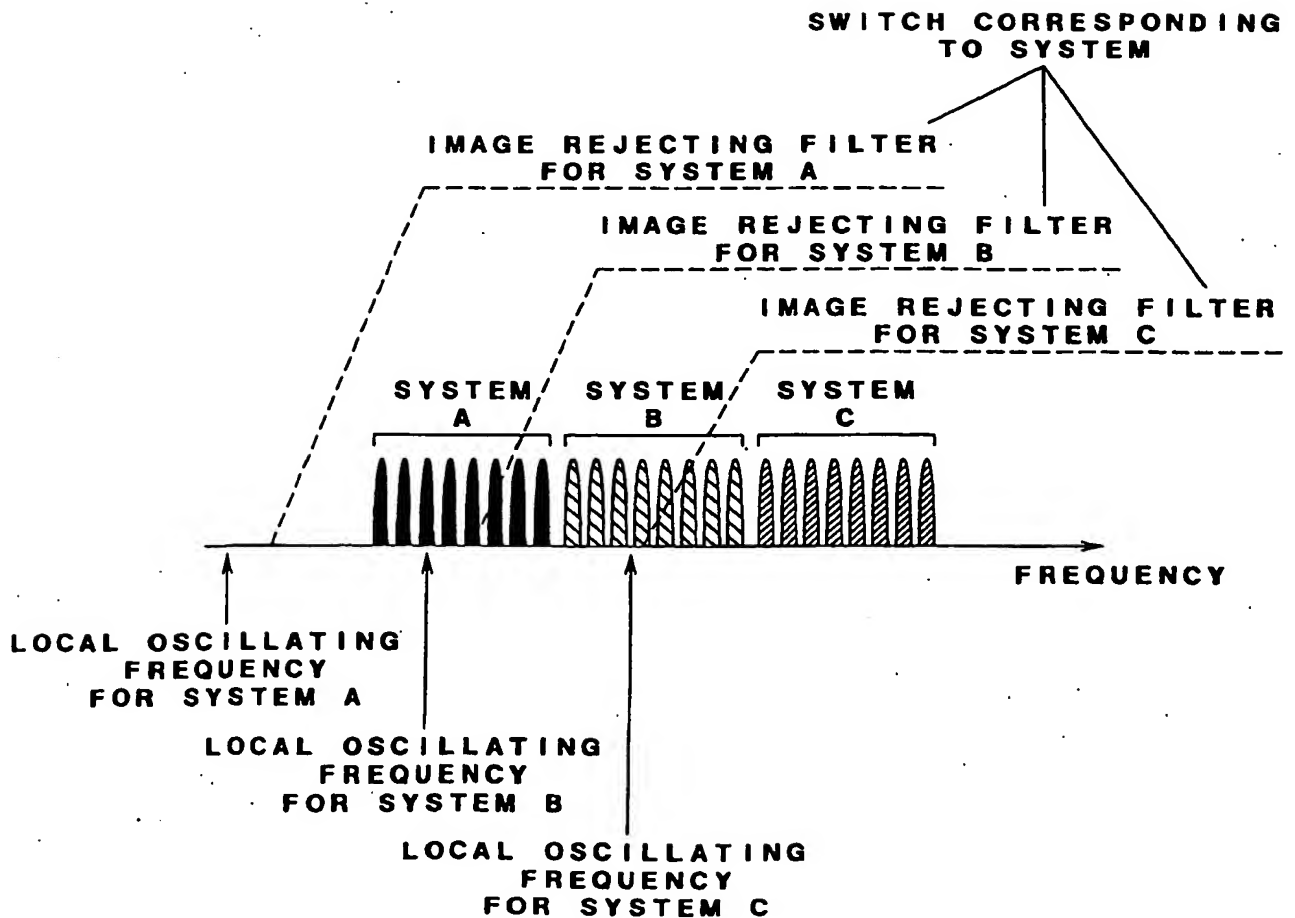


FIG. 21

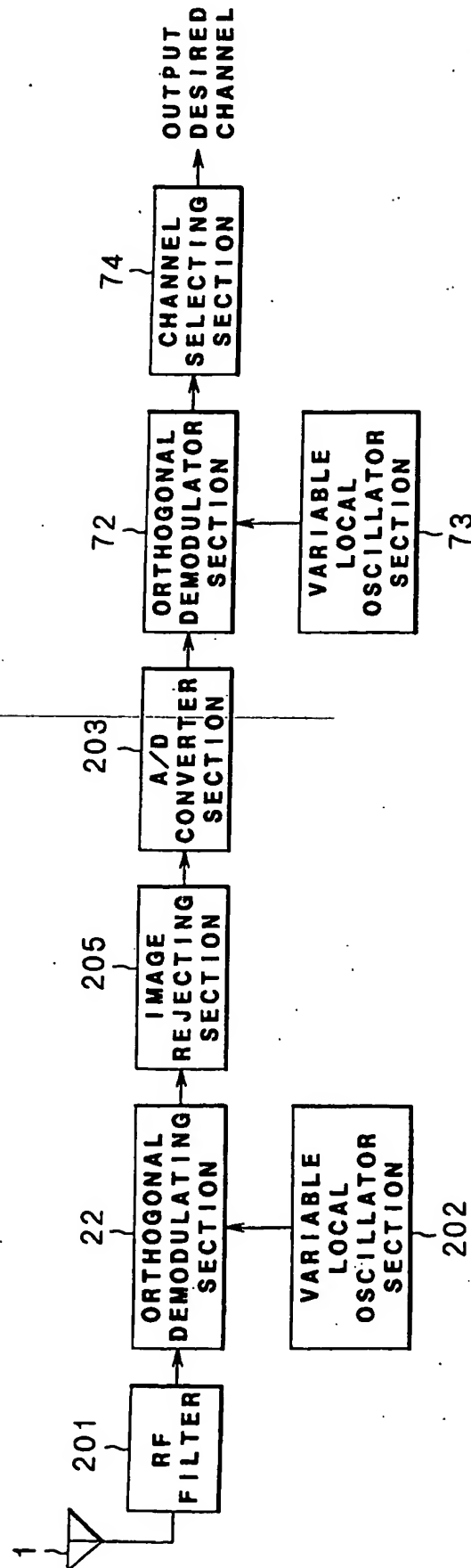


FIG. 22

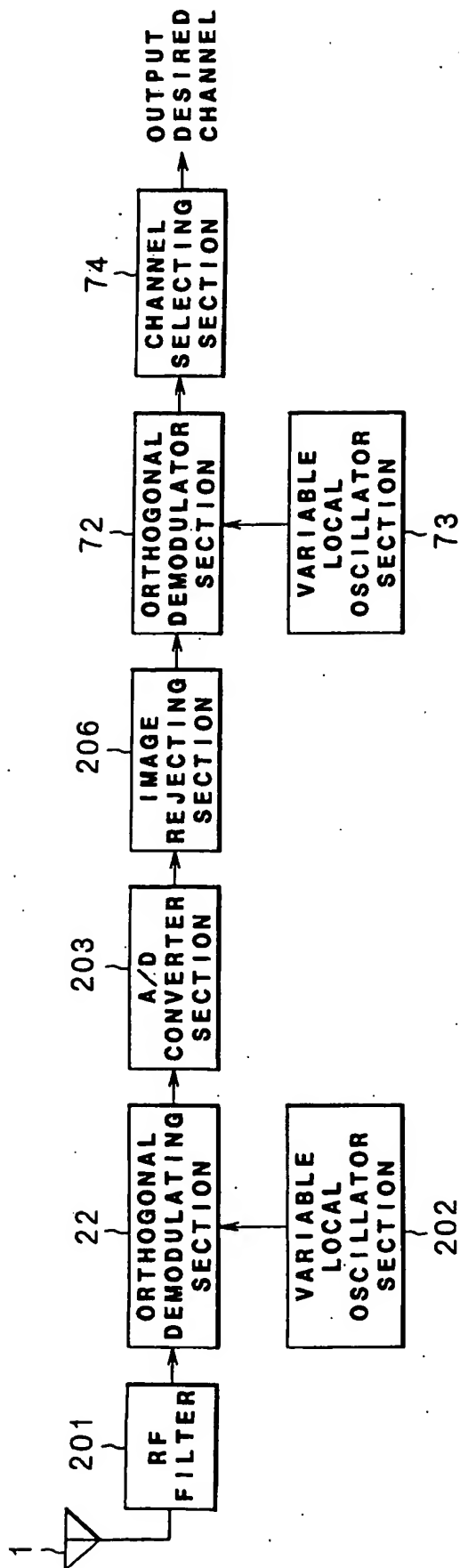




FIG. 23

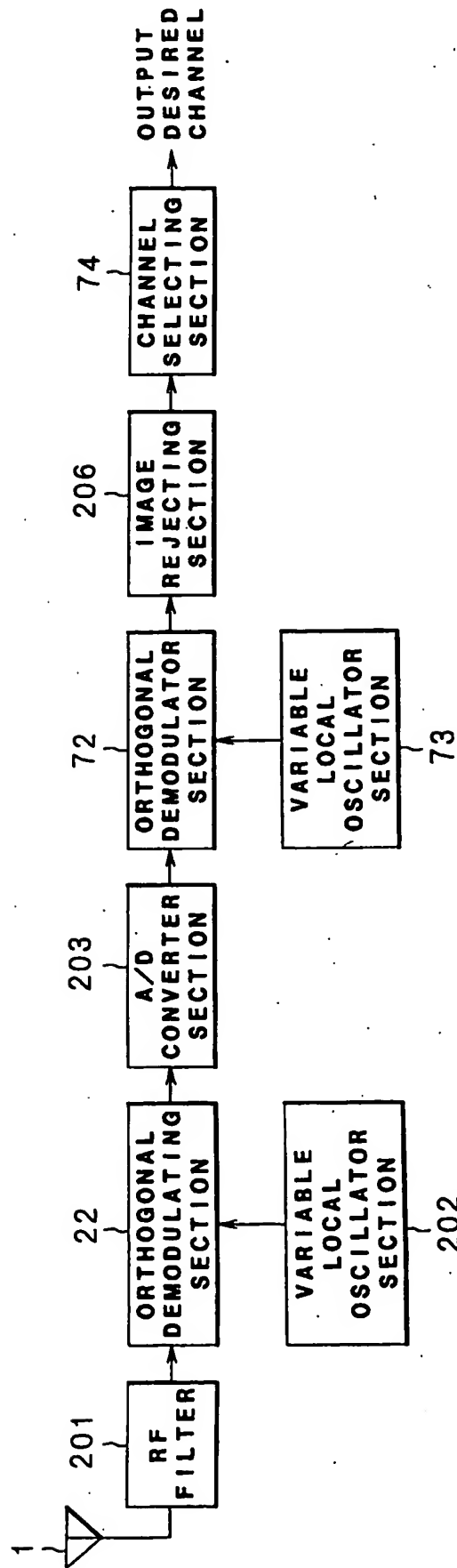
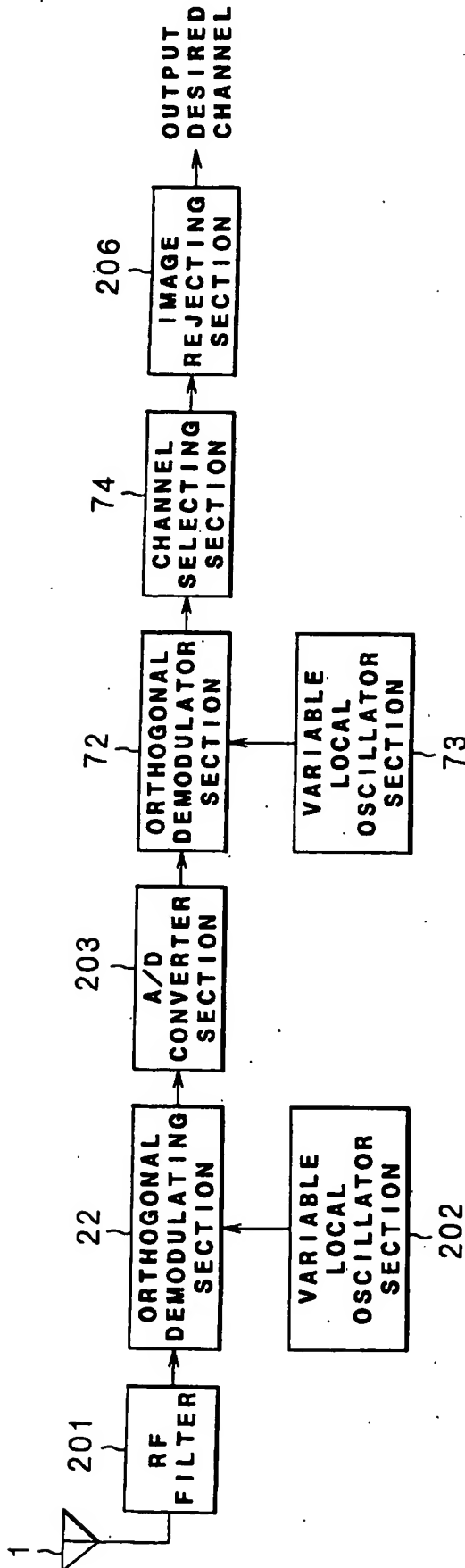
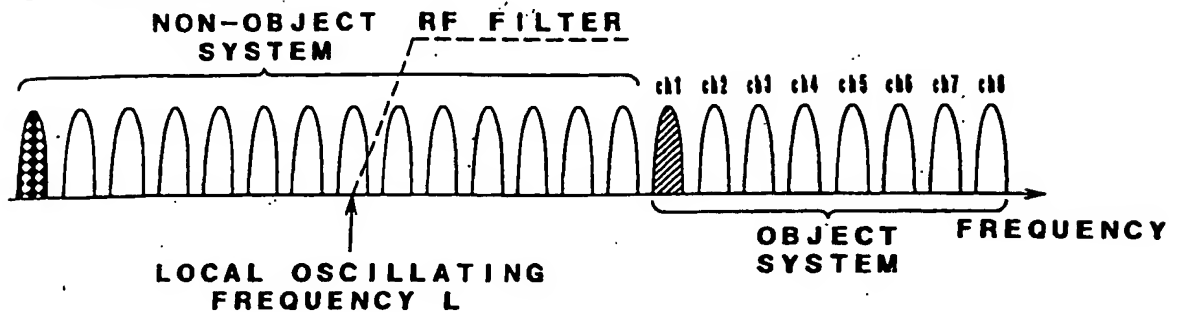


FIG. 24

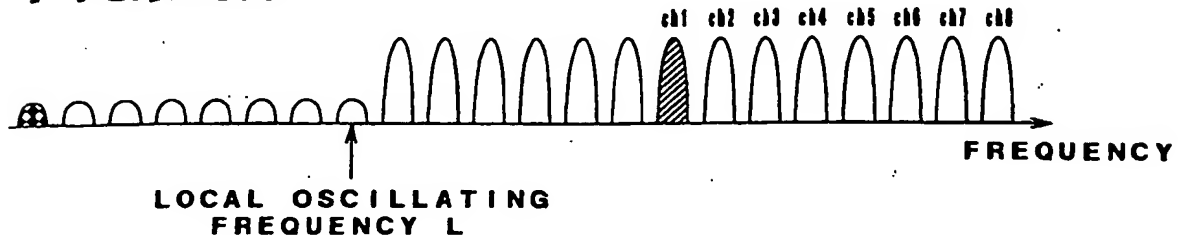


**FIG.25A**



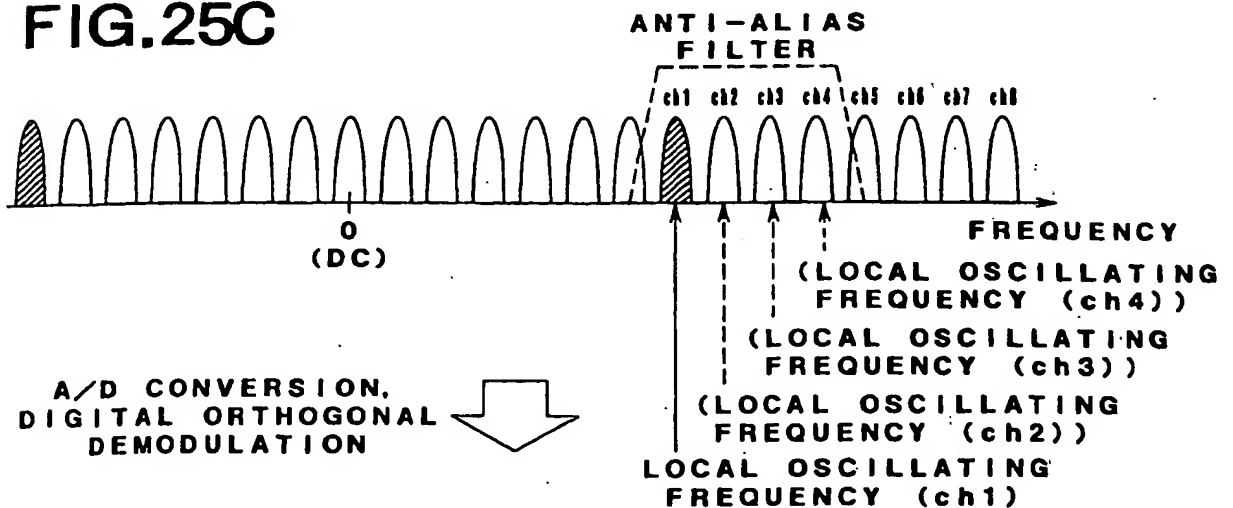
↓ REJECTING IMAGE BY RF FILTER

**FIG.25B**



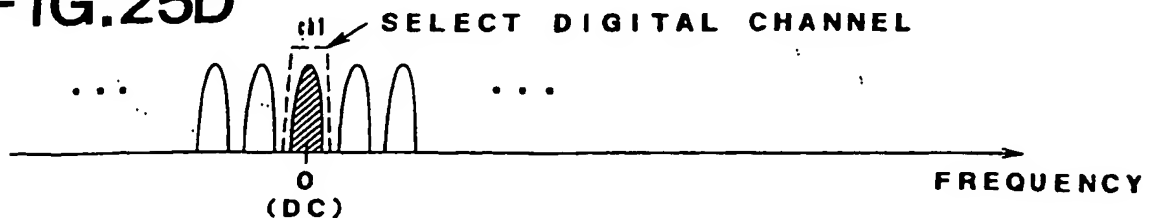
↓ ANALOG ORTHOGONAL DEMODULATION AND IMAGE REJECTION

**FIG.25C**

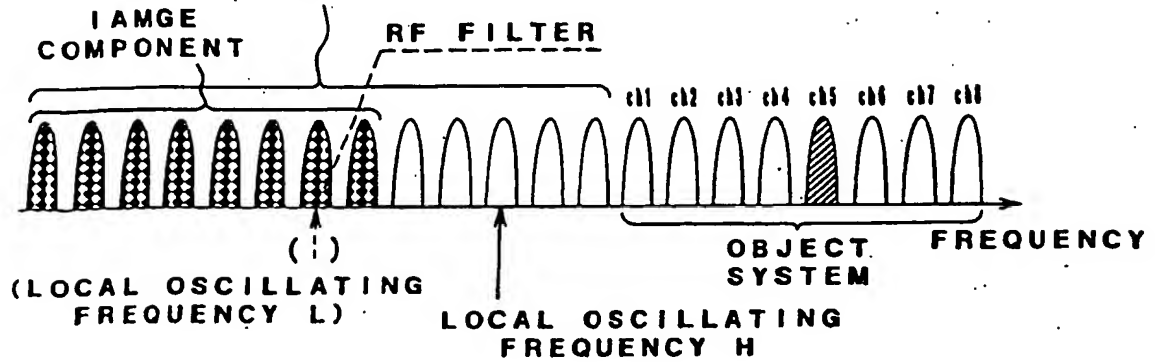


↓ A/D CONVERSION, DIGITAL ORTHOGONAL DEMODULATION

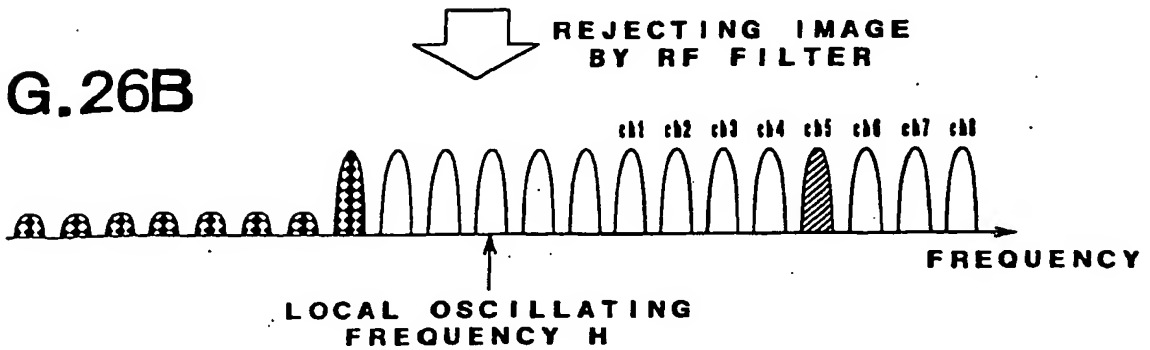
**FIG.25D**



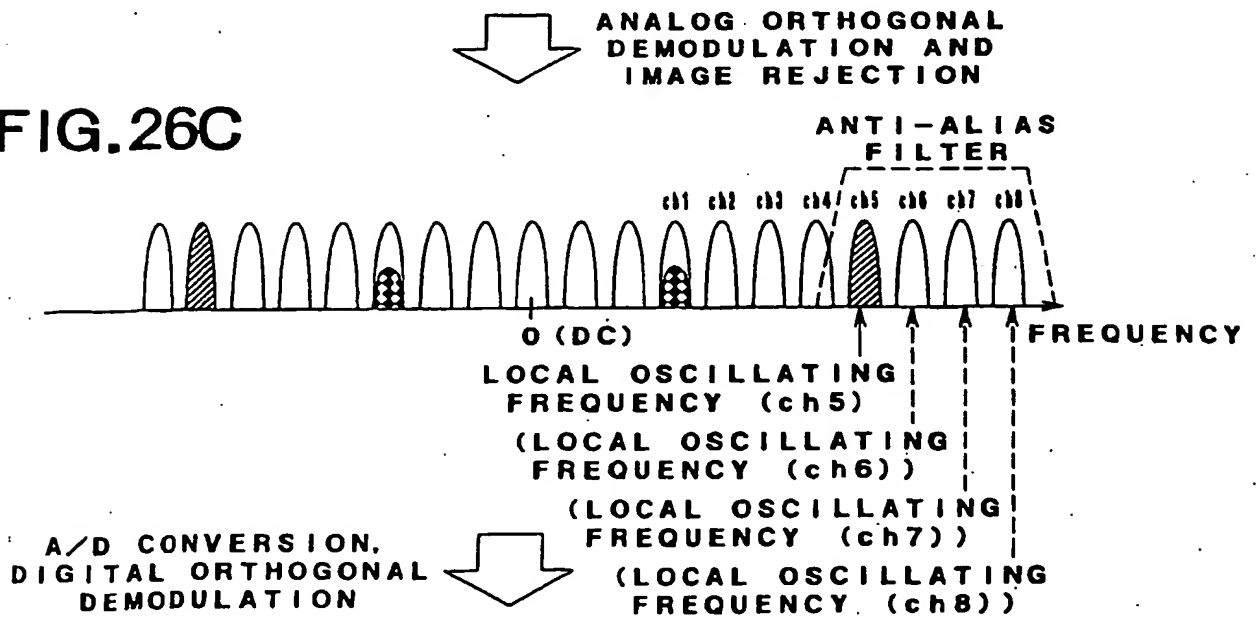
**FIG. 26A** NON-OBJECT SYSTEM



**FIG. 26B**



**FIG. 26C**



**FIG. 26D**

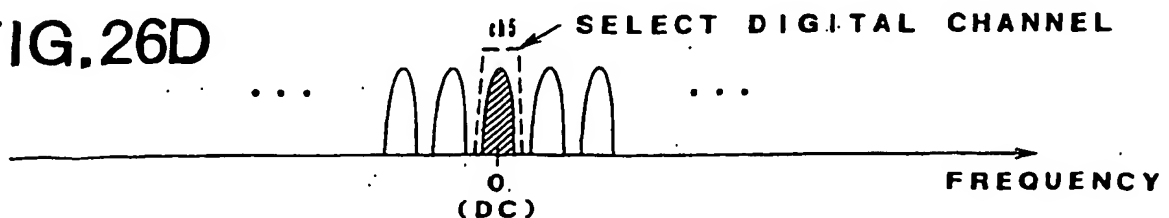


FIG.27

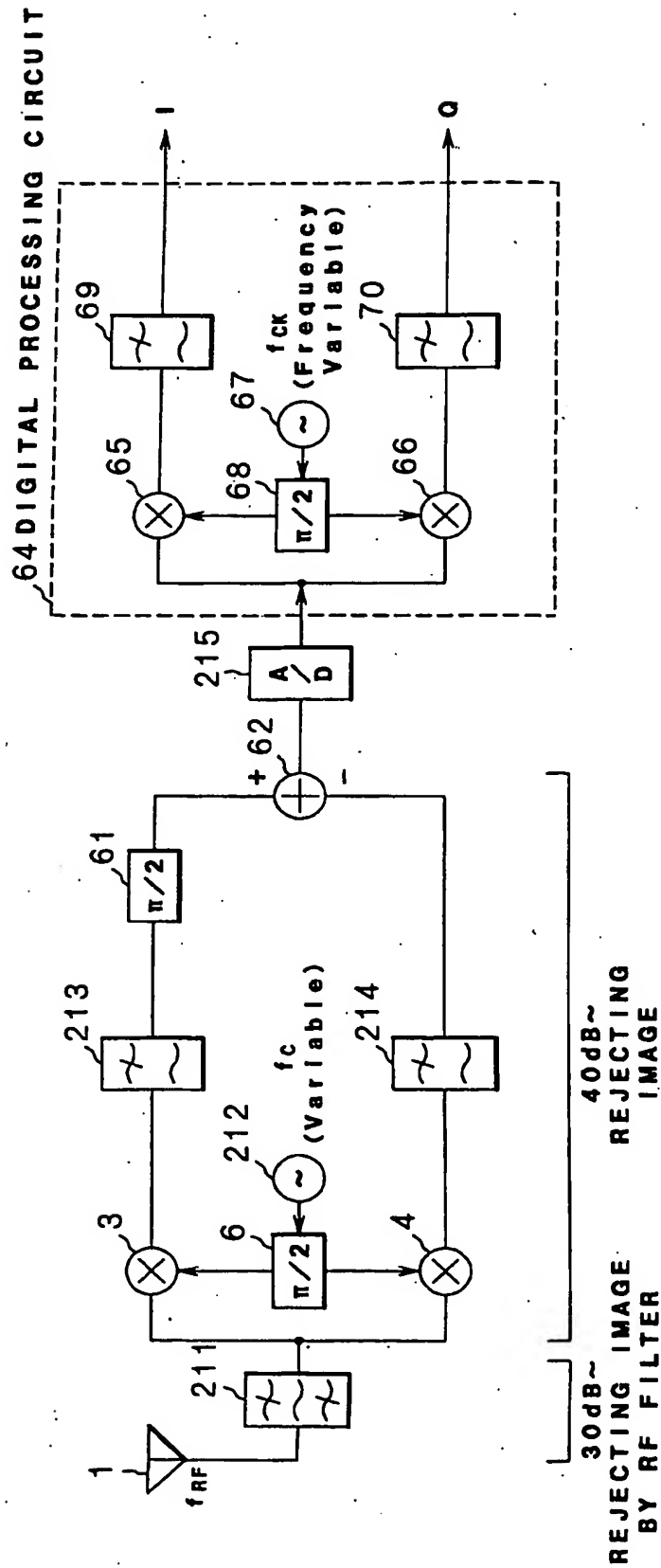


FIG.28

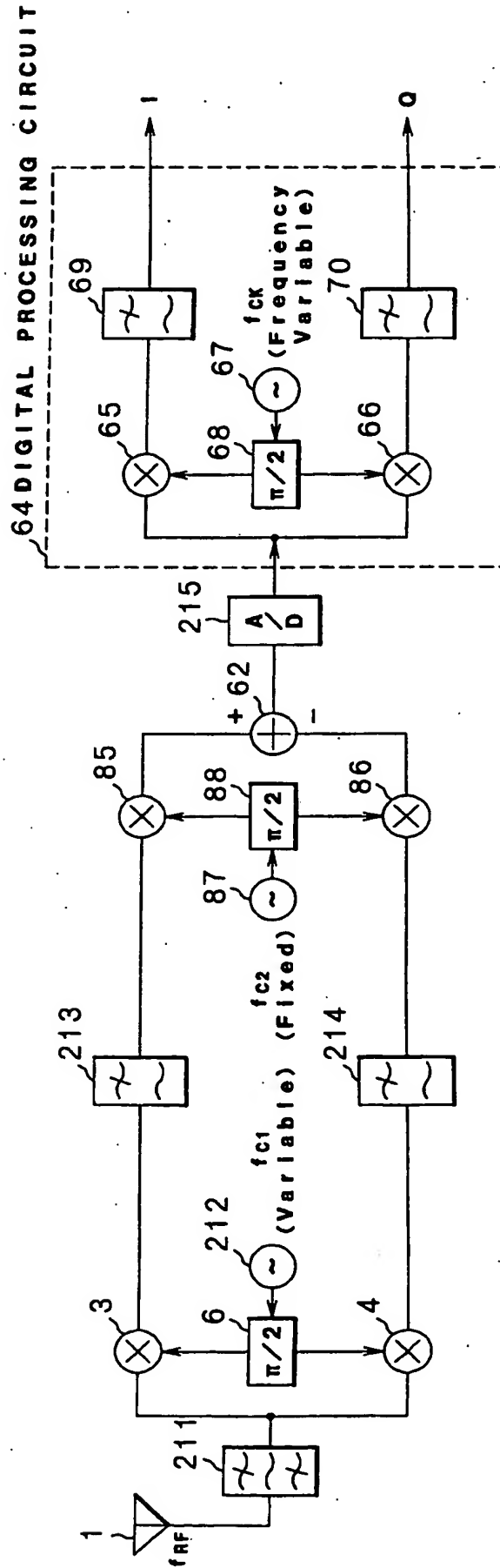


FIG.29

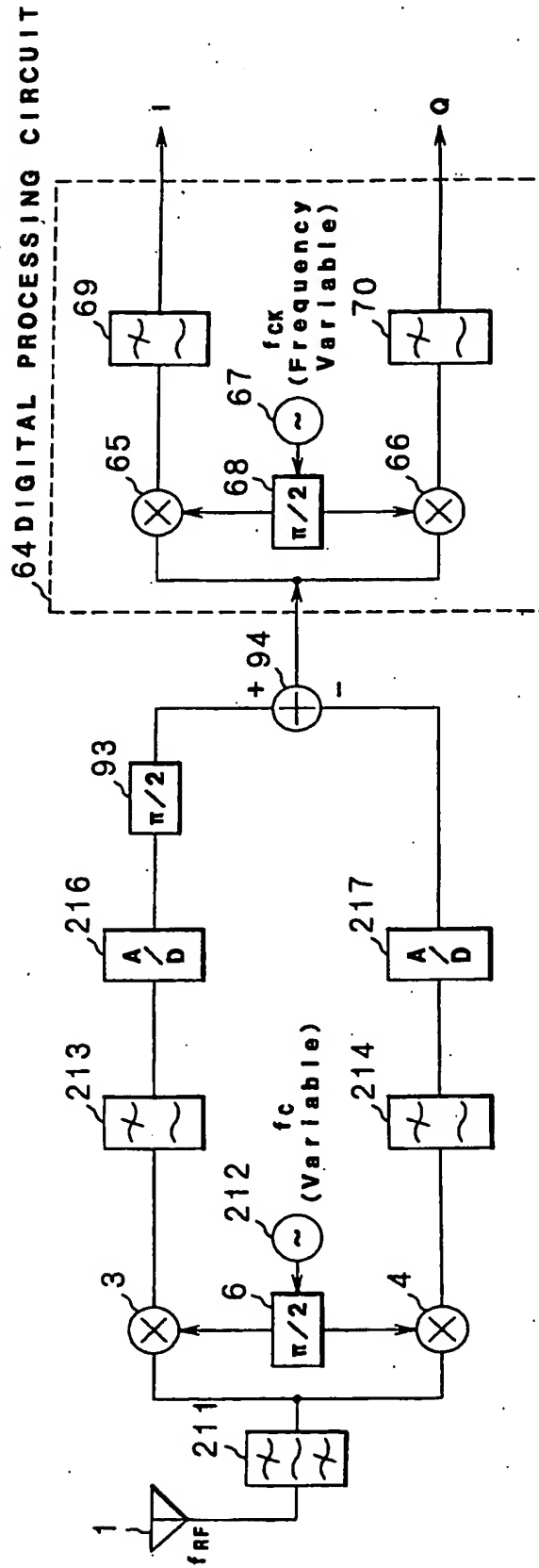


FIG.30

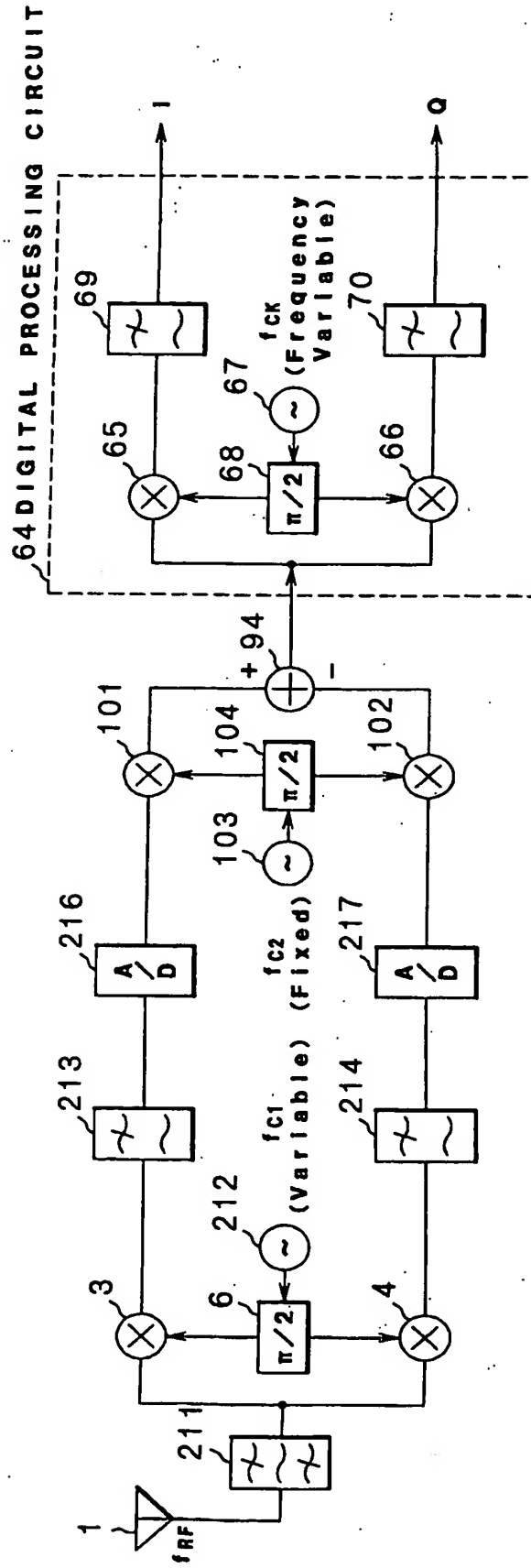




FIG.31

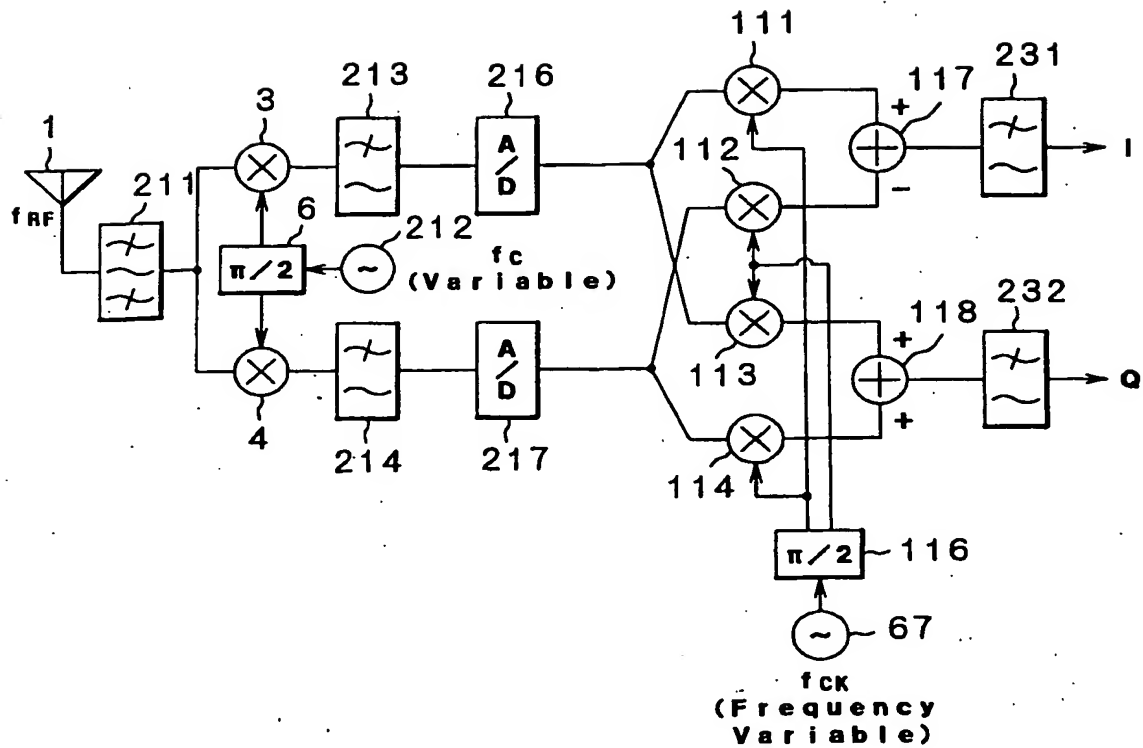


FIG.32

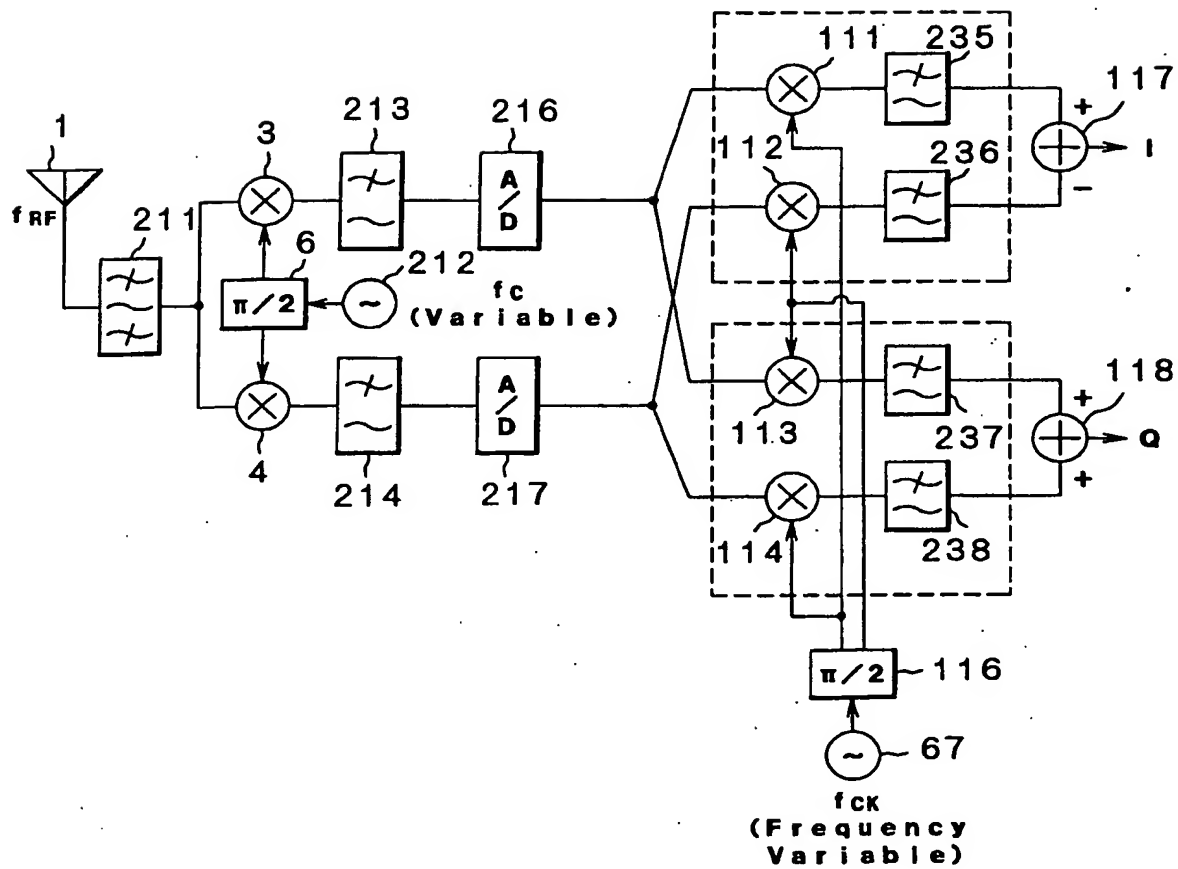


FIG. 33

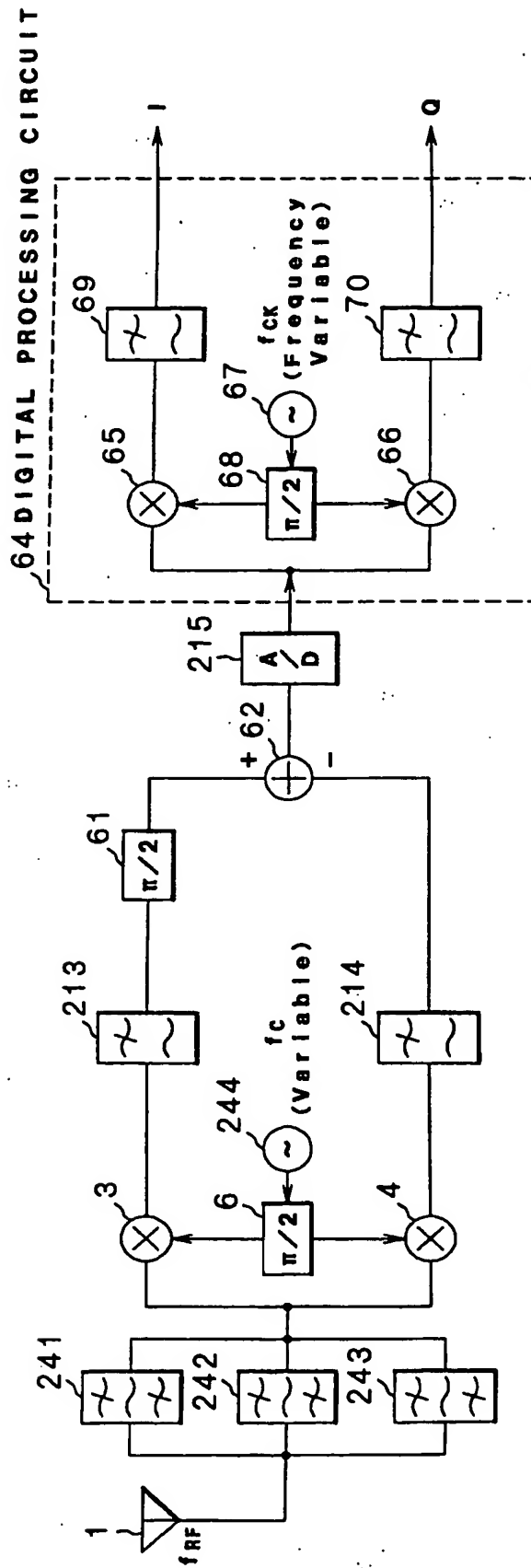


FIG.34

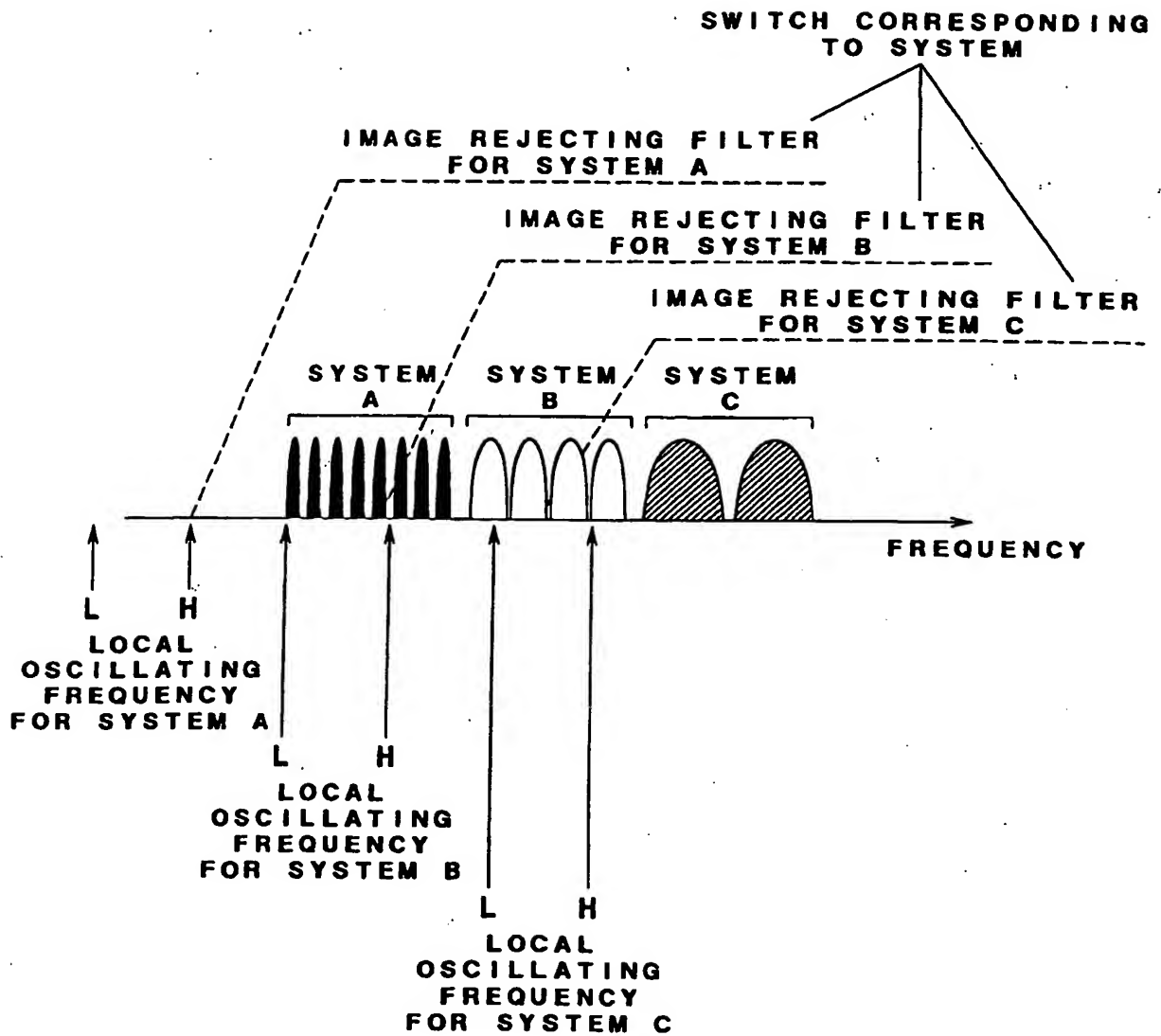
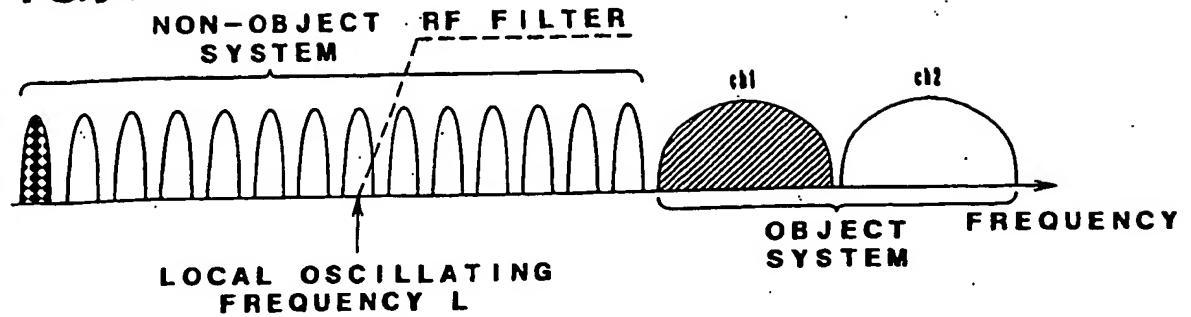
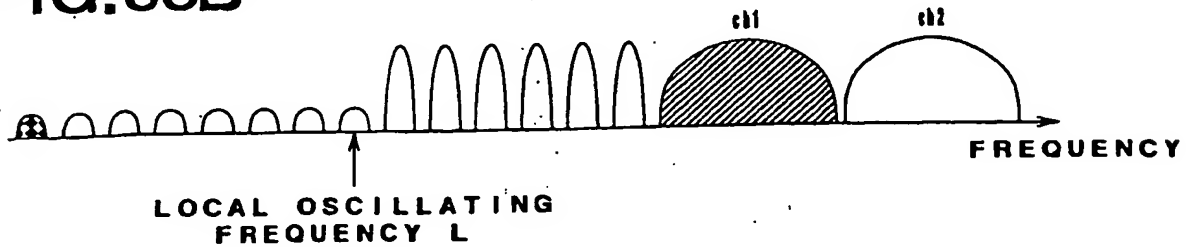


FIG.35A



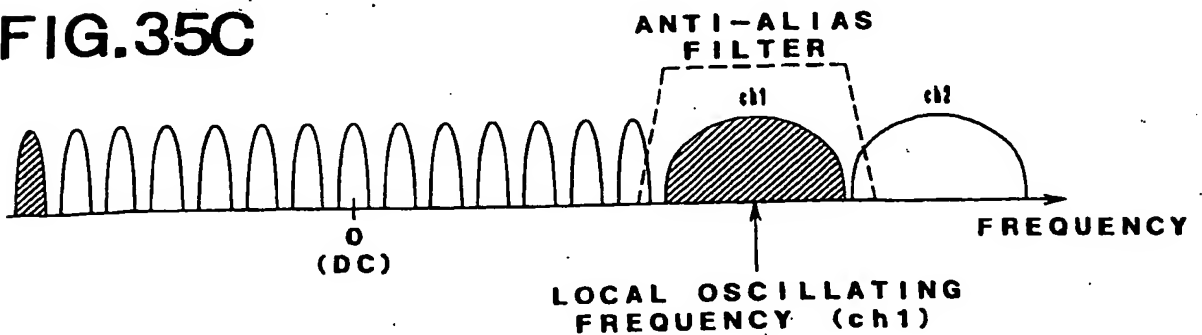
REJECTING IMAGE  
BY RF FILTER

FIG.35B



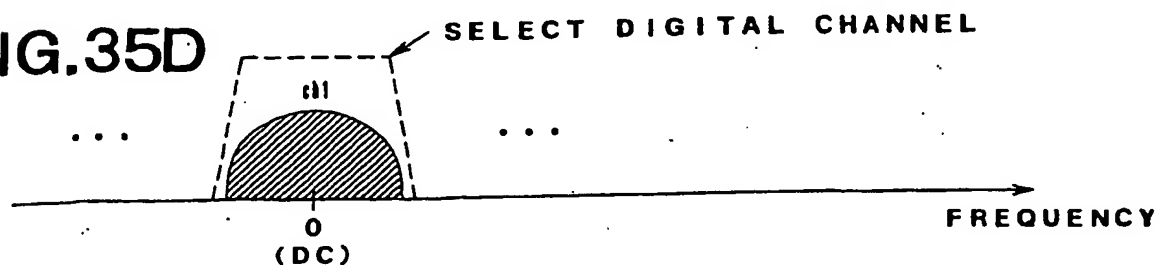
ANALOG ORTHOGONAL  
DEMULATION AND  
IMAGE REJECTION

FIG.35C

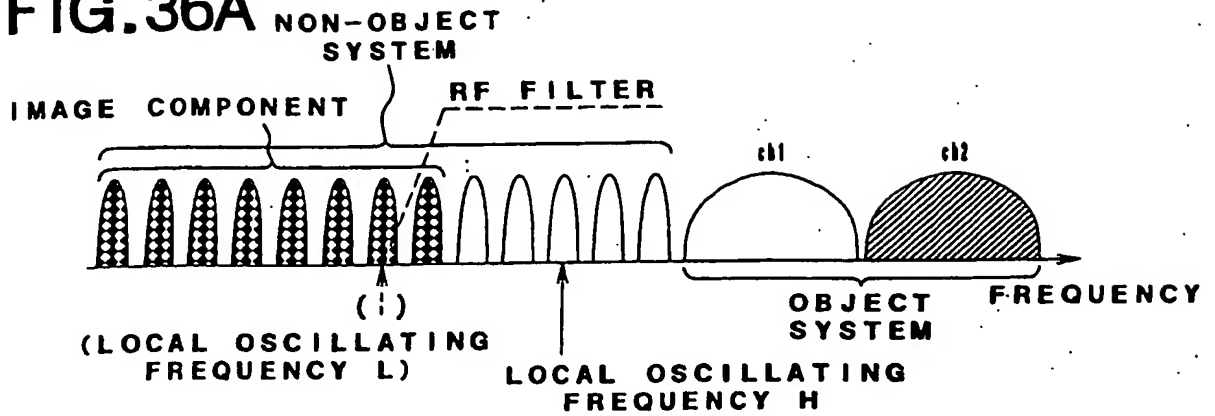


A/D CONVERSION,  
DIGITAL ORTHOGONAL  
DEMULATION

FIG.35D

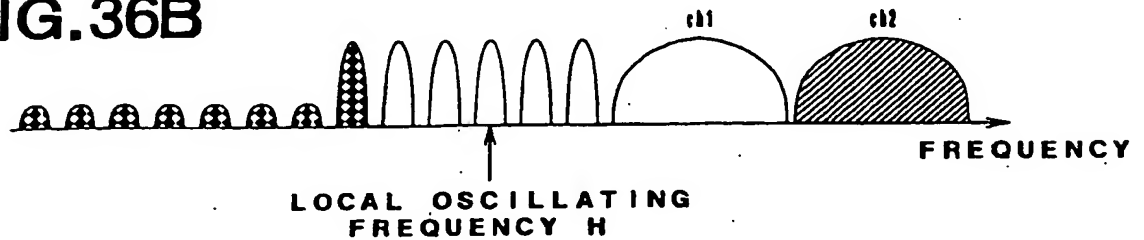


**FIG.36A**



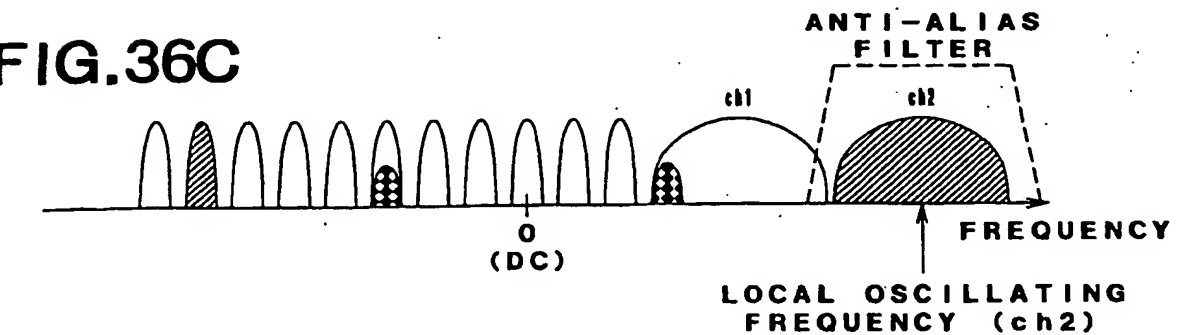
REJECTING IMAGE BY RF FILTER

**FIG.36B**



ANALOG ORTHOGONAL DEMODULATION AND IMAGE REJECTION

**FIG.36C**



A/D CONVERSION,  
 DIGITAL ORTHOGONAL  
 DEMODULATION

**FIG.36D**

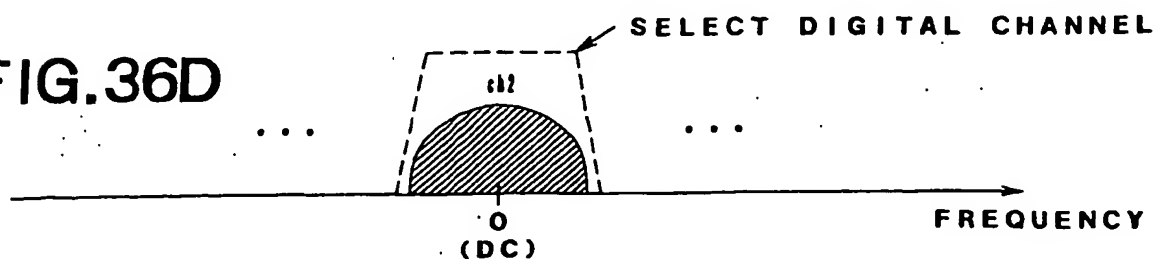
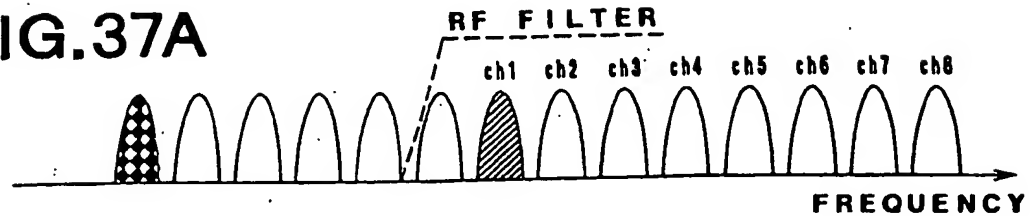
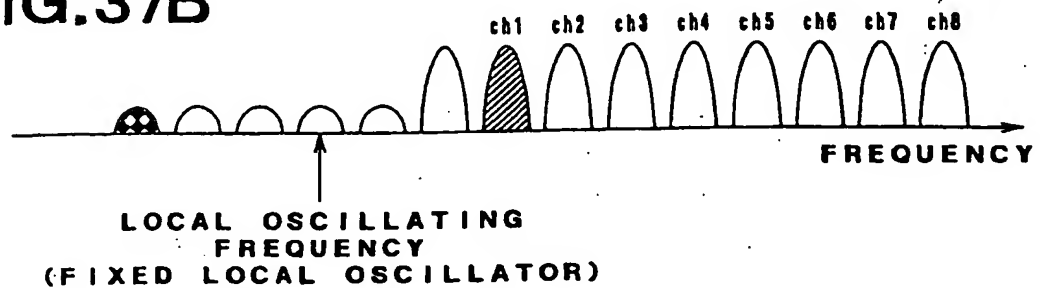


FIG.37A



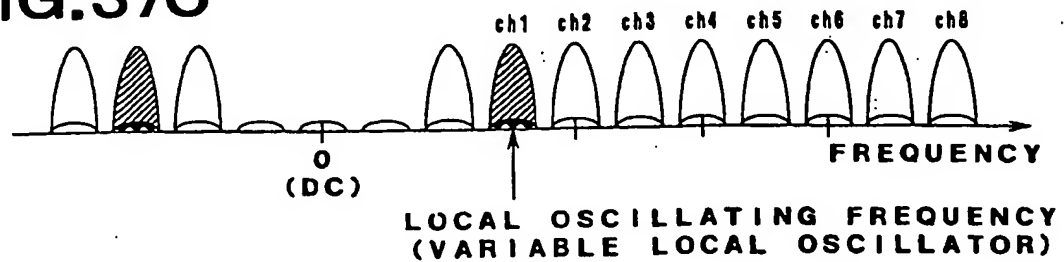
REJECTING IMAGE  
BY RF FILTER

FIG.37B



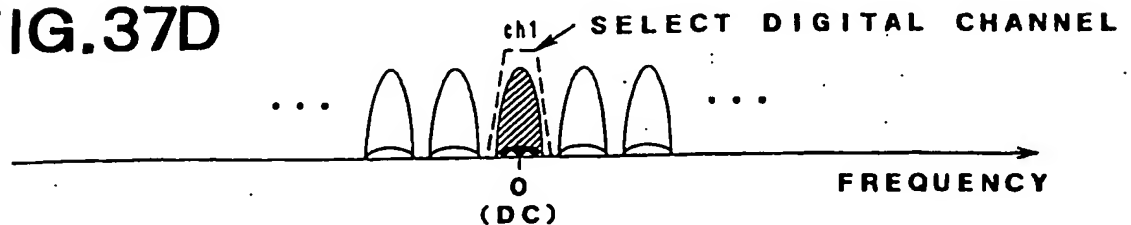
ANALOG ORTHOGONAL  
DEMODULATION,  
A/D CONVERSION,  
REJECTING IMAGE

FIG.37C

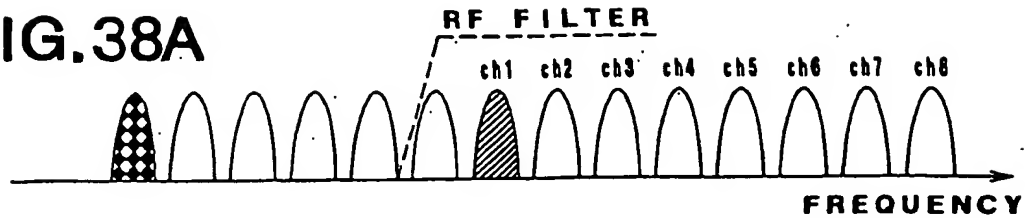


DIGITAL ORTHOGONAL  
DEMODULATION

FIG.37D

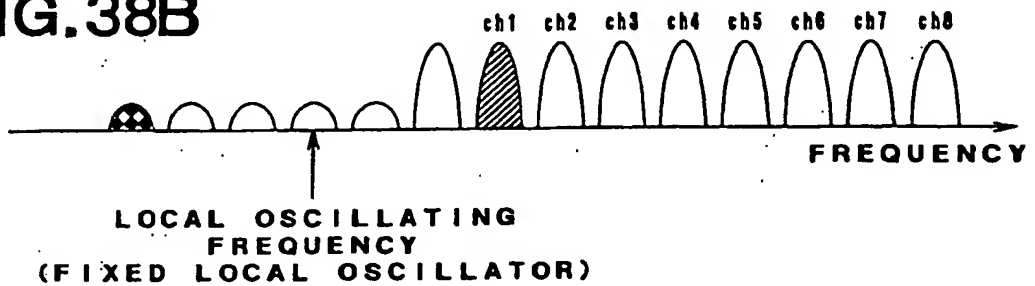


**FIG.38A**



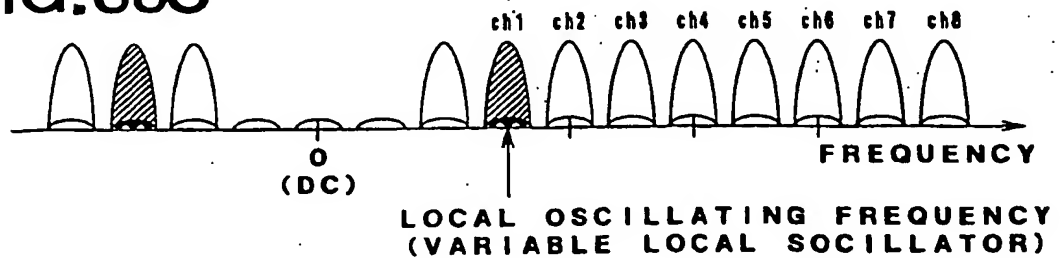
↓  
REJECTING IMAGE  
BY RF FILTER

**FIG.38B**



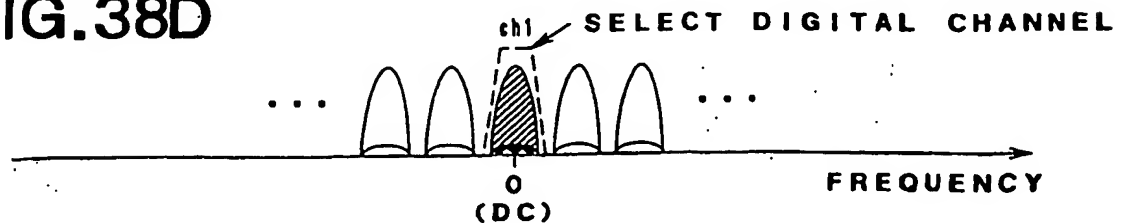
↓  
ANALOG ORTHOGONAL  
DEMODULATION,  
A/D CONVERSION

**FIG.38C**



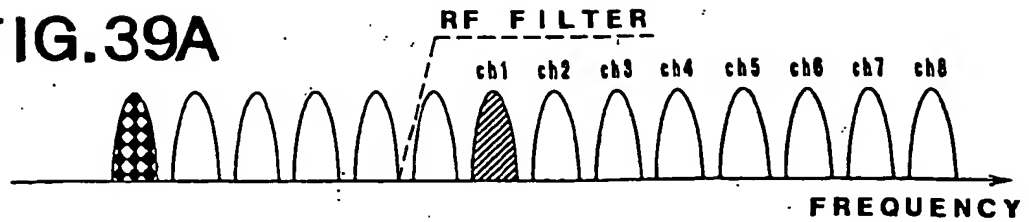
↓  
DIGITAL ORTHOGONAL  
DEMODULATION,  
REJECTING IMAGE

**FIG.38D**



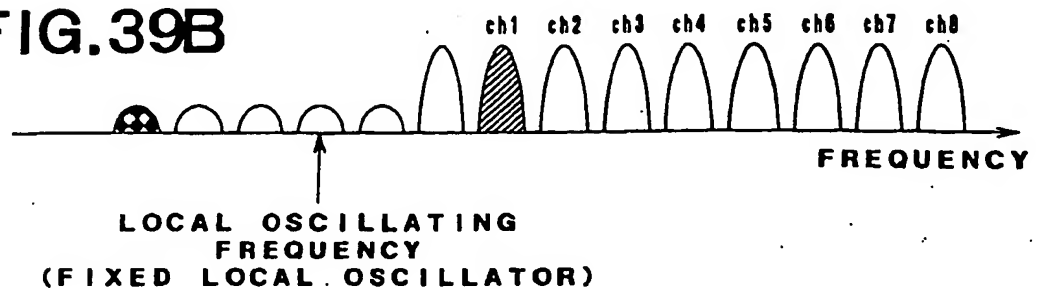


**FIG.39A**



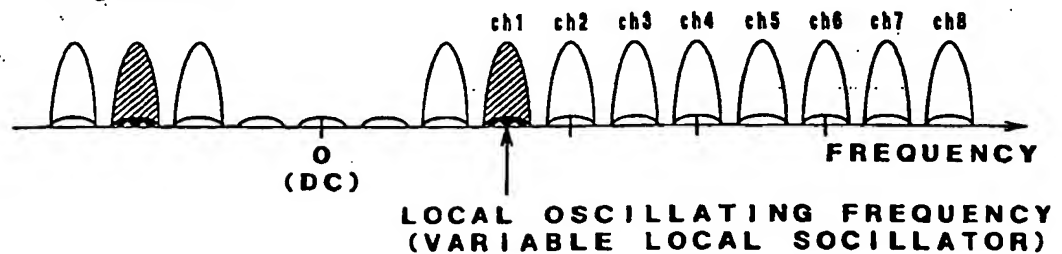
REJECTING IMAGE  
BY RF FILTER

**FIG.39B**



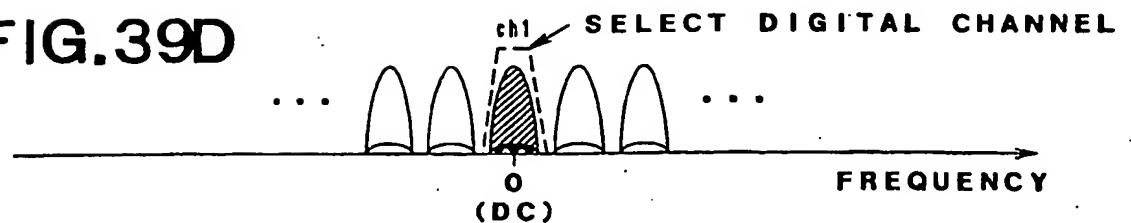
ANALOG ORTHOGONAL  
DEMULATION,  
A/D CONVERSION

**FIG.39C**



DIGITAL ORTHOGONAL  
DEMULATION

**FIG.39D**



REJECTING IMAGE

**FIG.39E**

